

# DisCarte: A Disjunctive Internet Cartographer

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# Introduction

- ◆ Network maps are useful
- ◆ Existing maps are small, stale, or inaccurate
- ◆ Mapping well requires:
  - lots of measurements (lots of error)
  - many techniques (lots of disagreement)



# Contributions

- ◆ Show how to use Record Route
- ◆ DisCarte
  - Cross-validates measurements to reduce error
- ◆ Introduce disjunctive logic programming (DLP)
  - Develop divide and conquer scheme to scale DLP to Internet-sized topologies
- ◆ Validate DisCarte against public topologies

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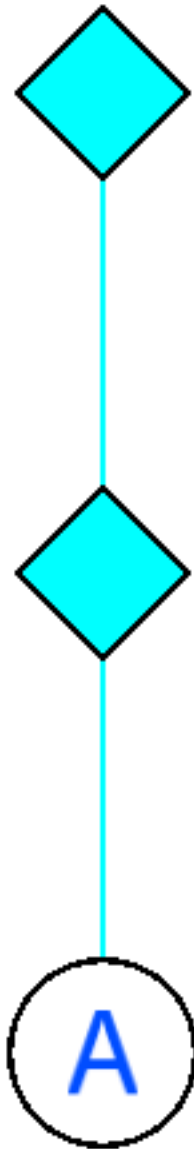
# Topology Inference



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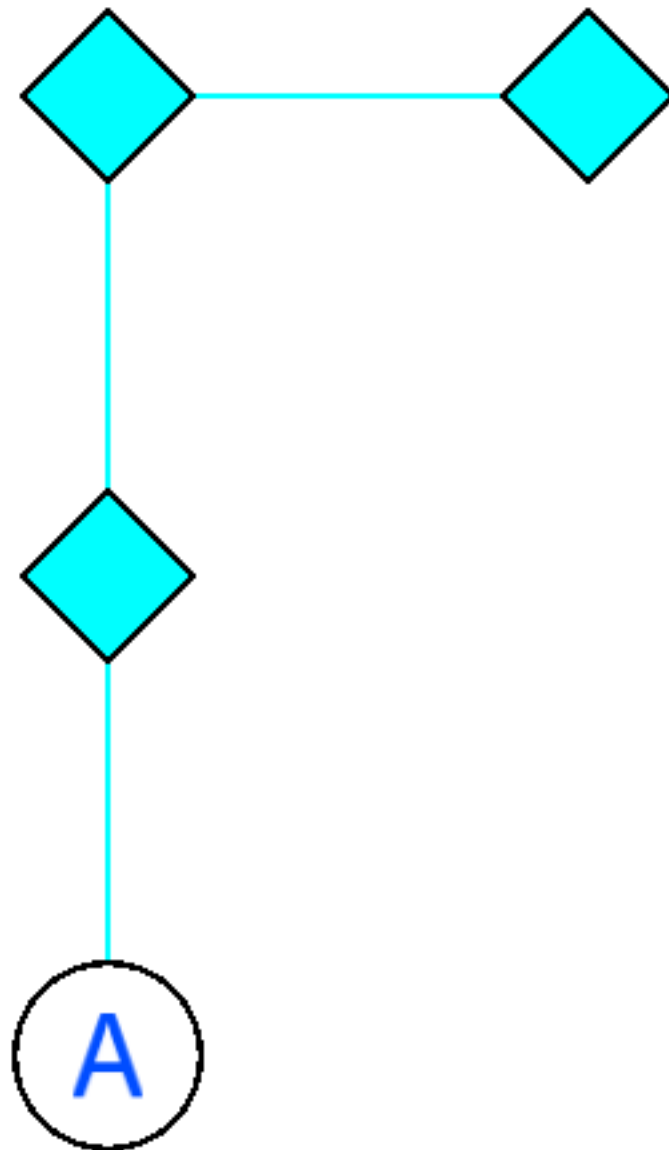


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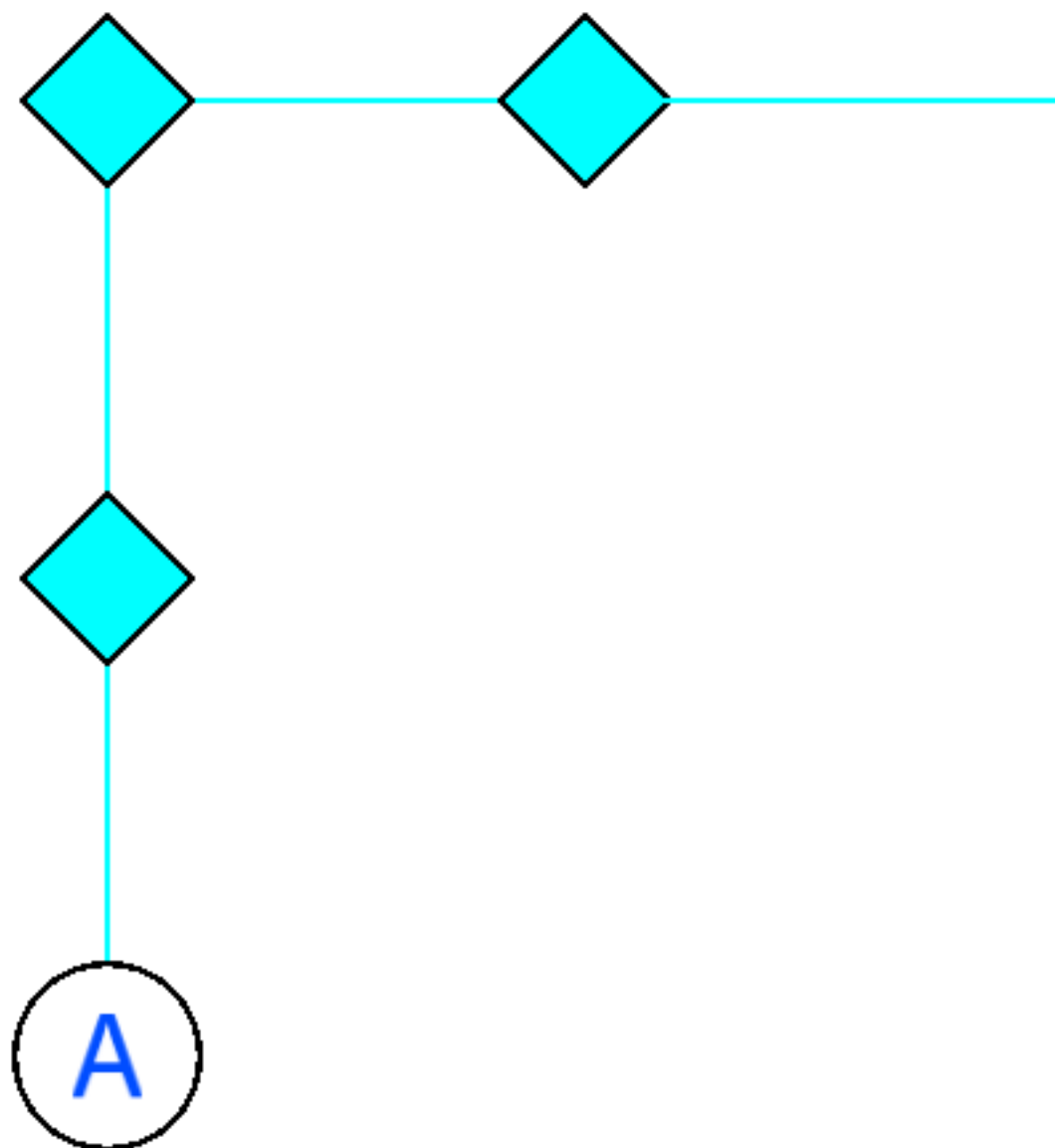




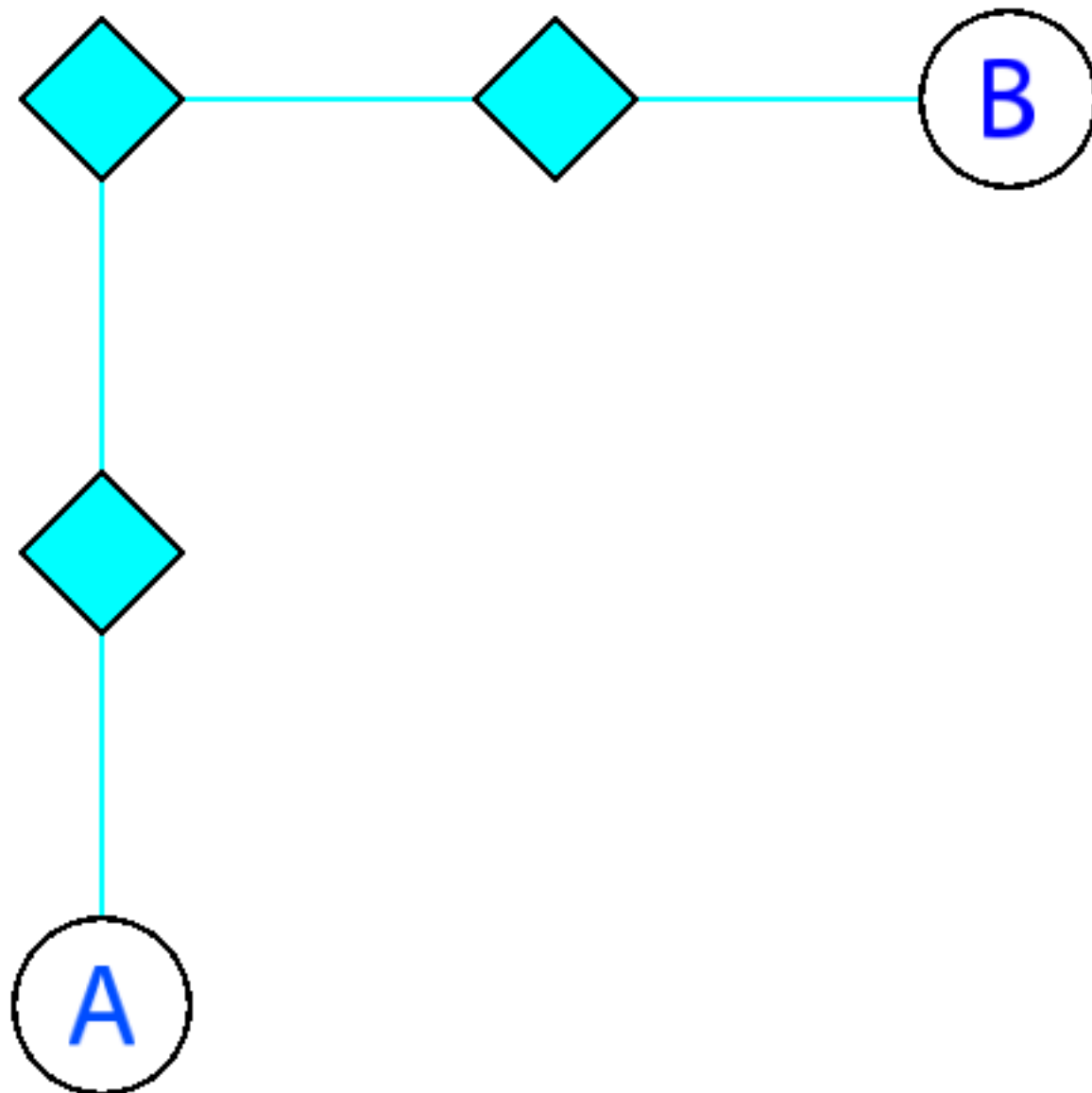
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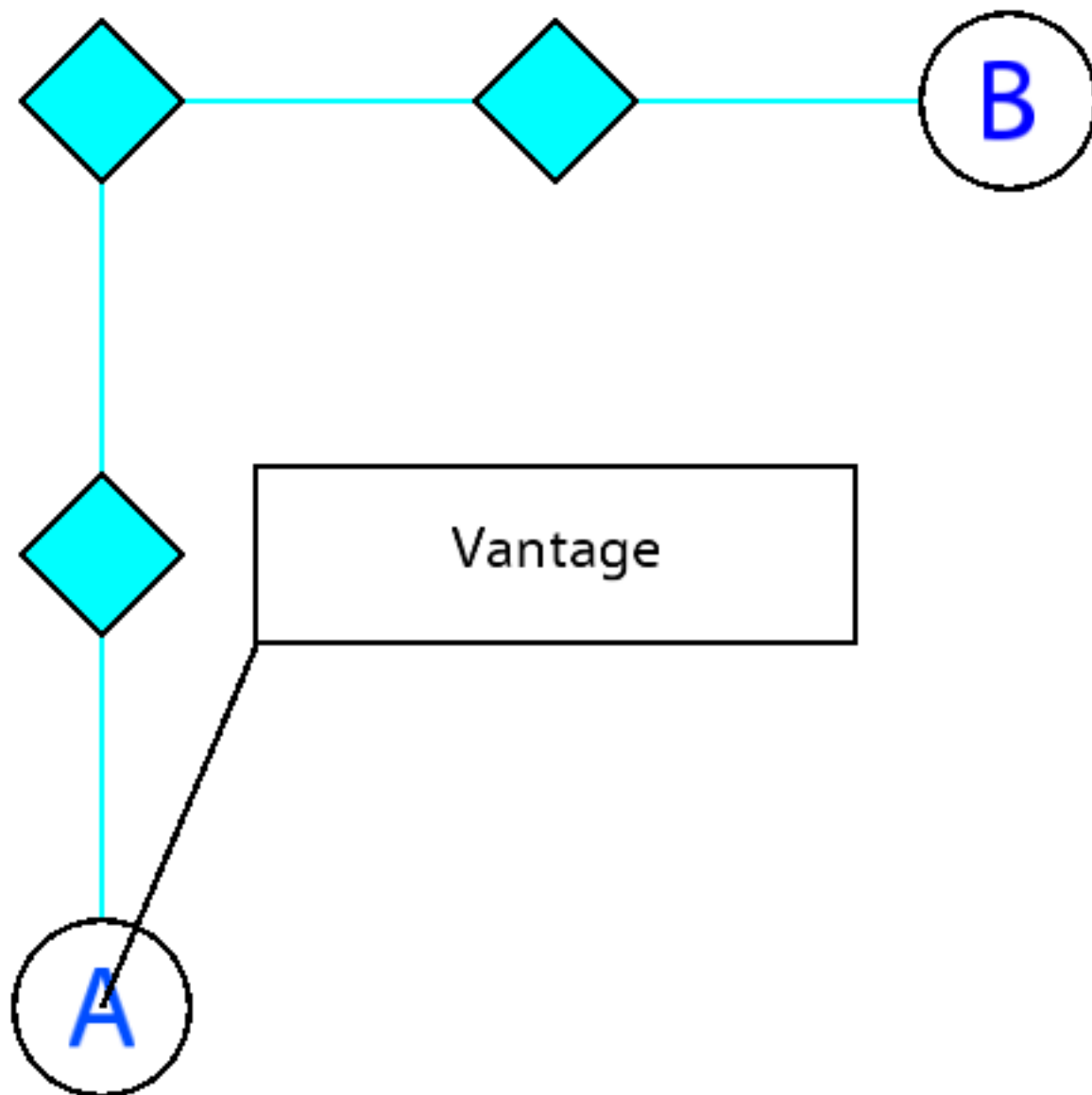
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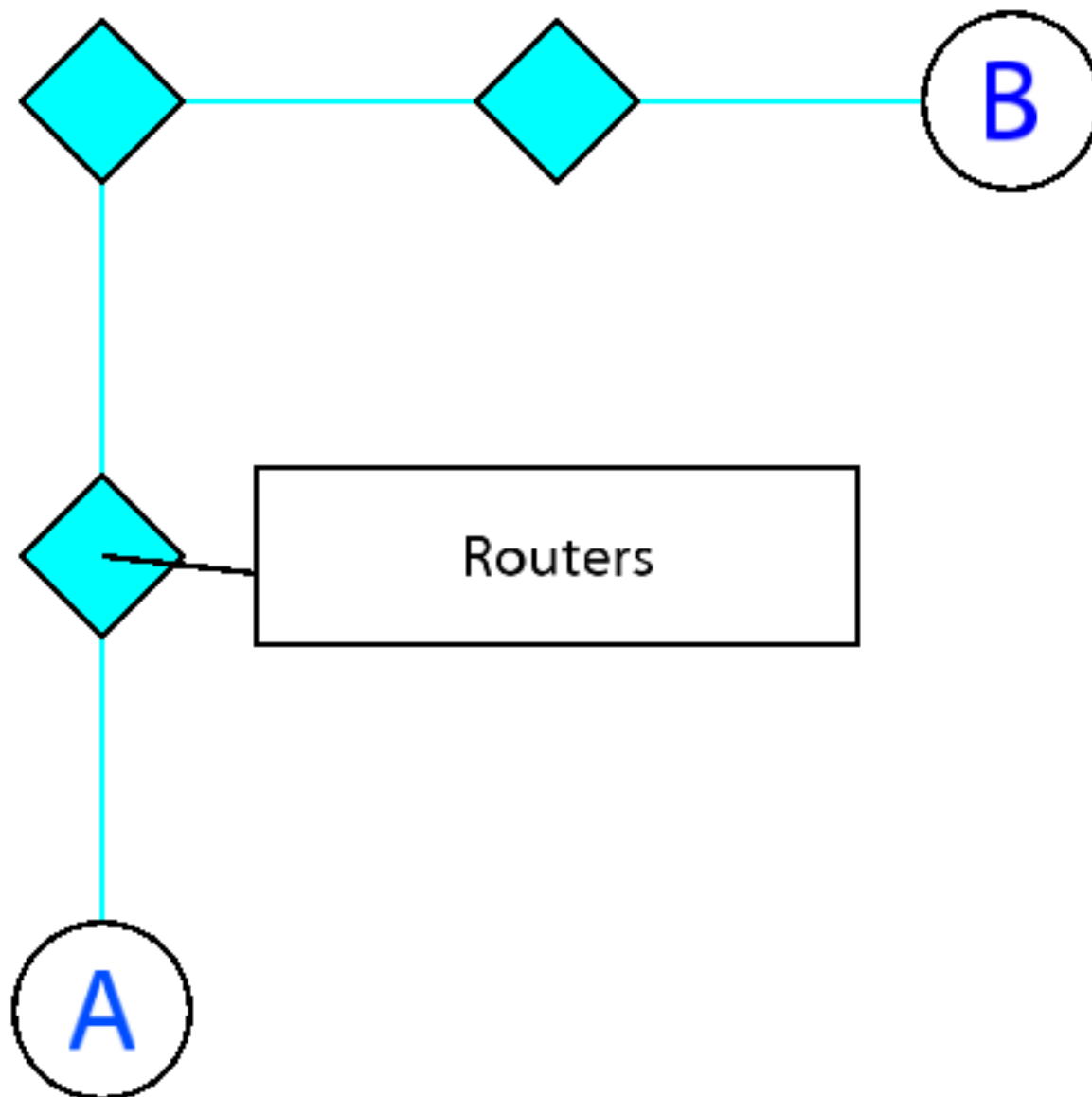
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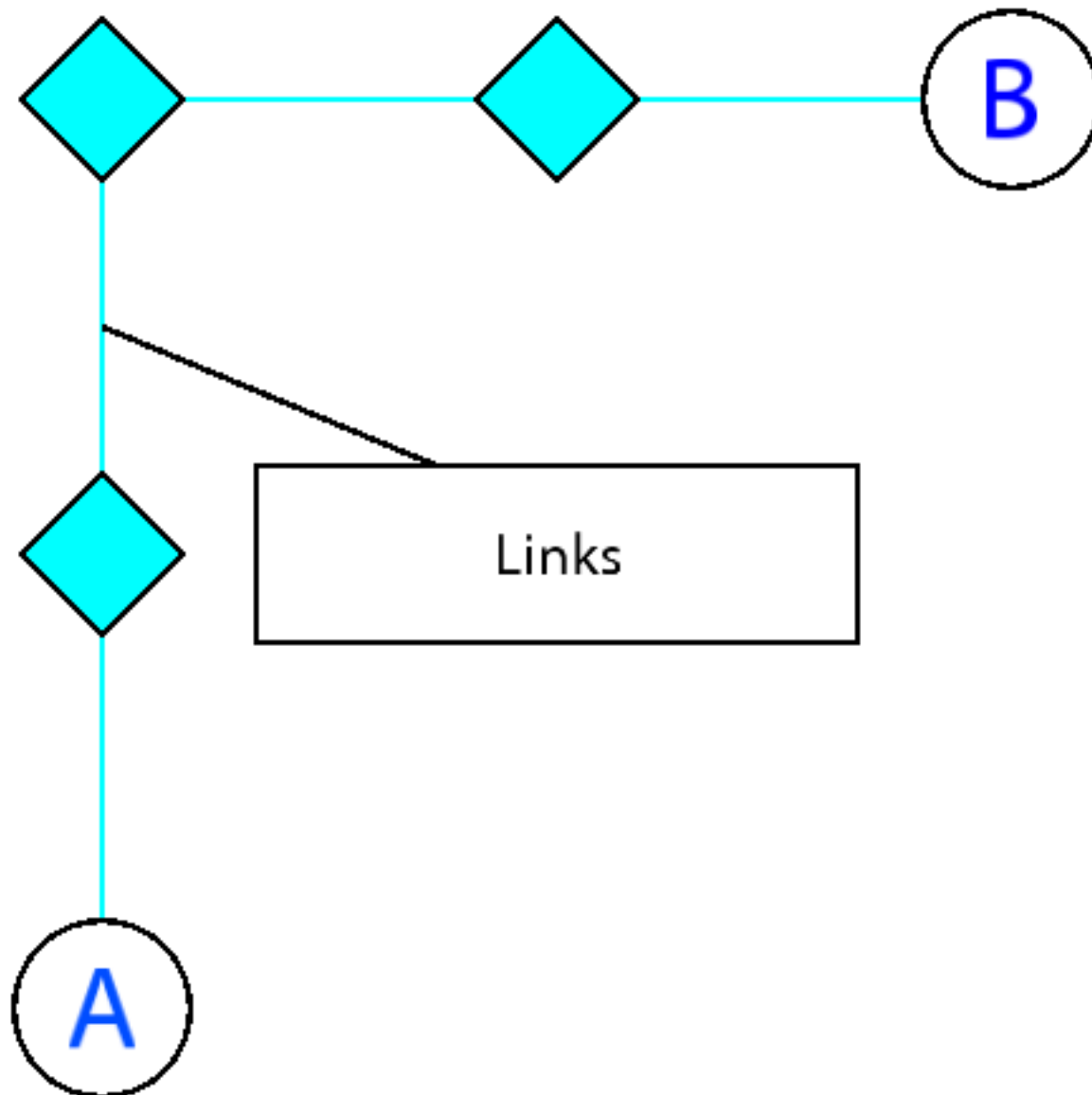
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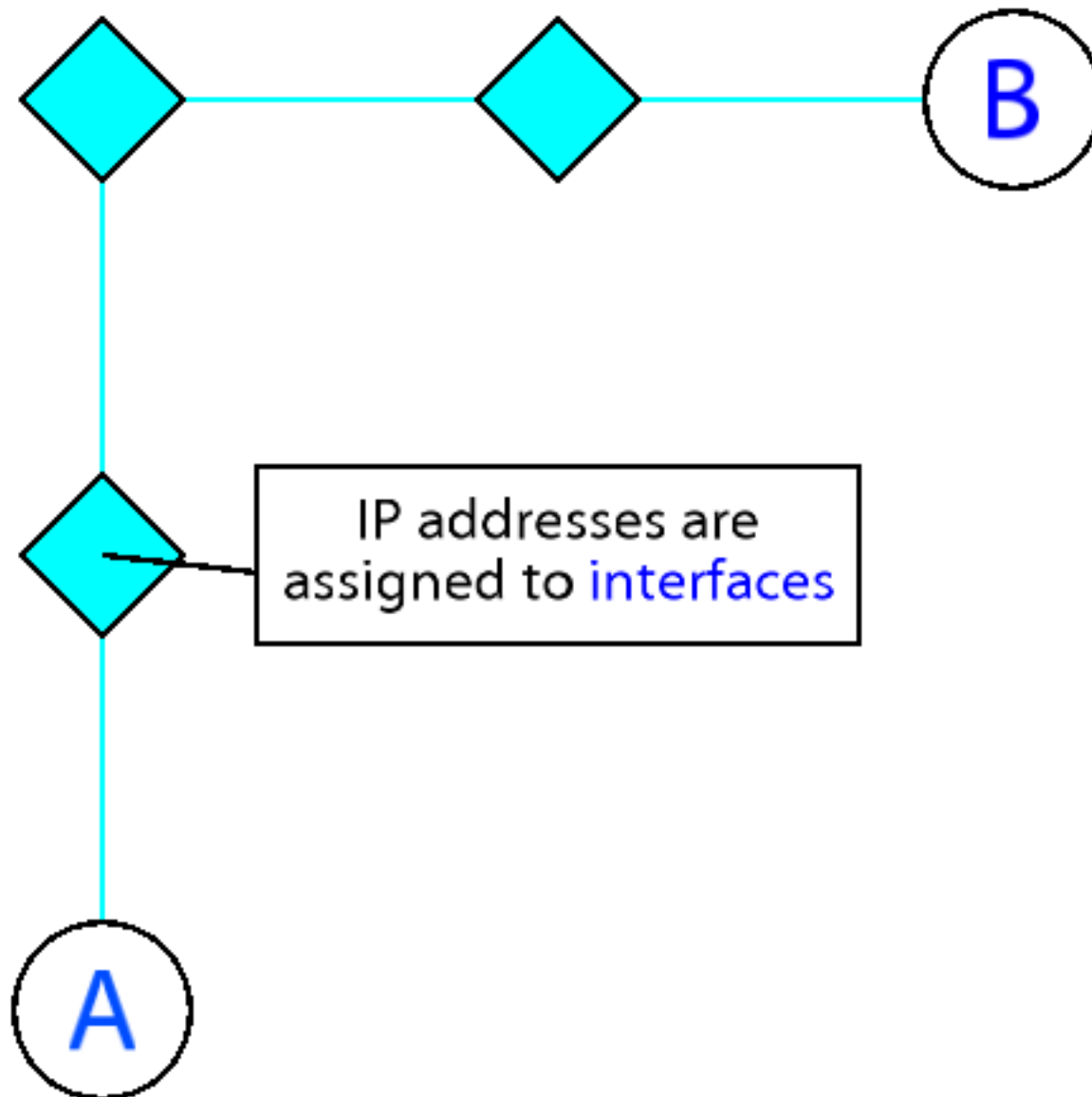
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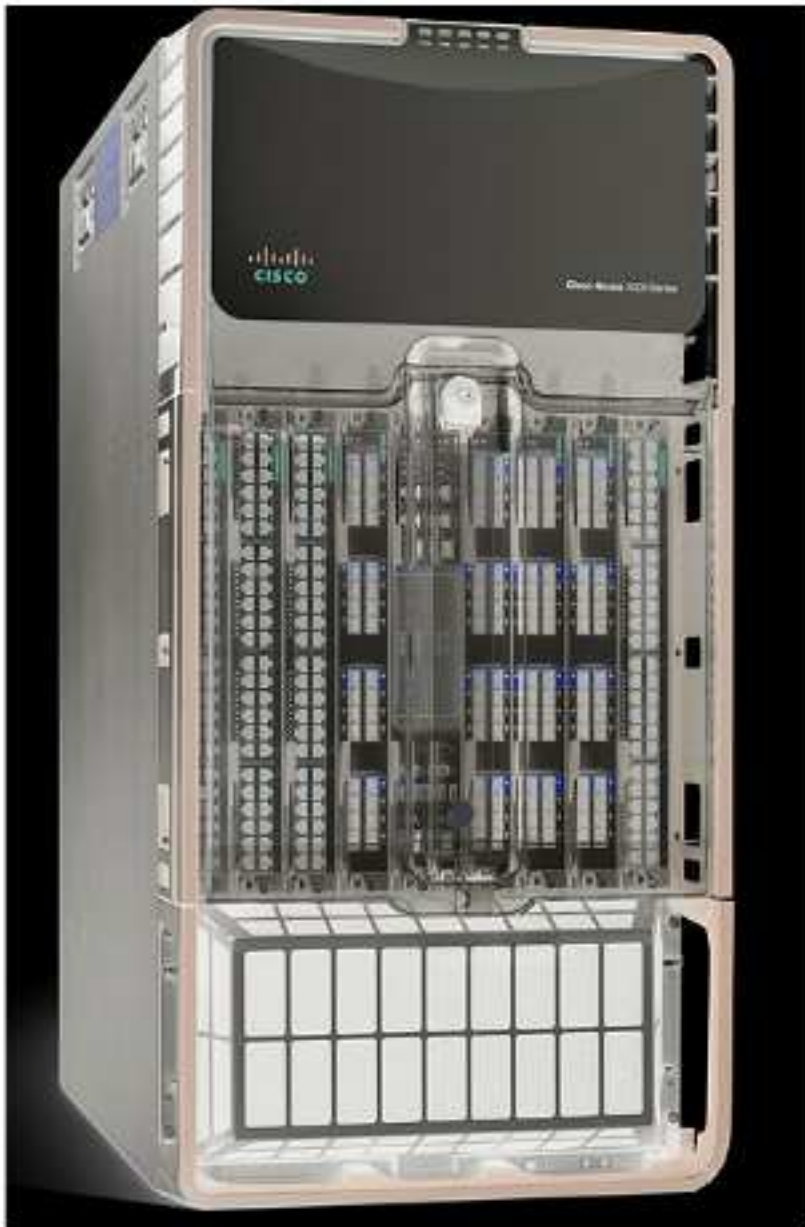
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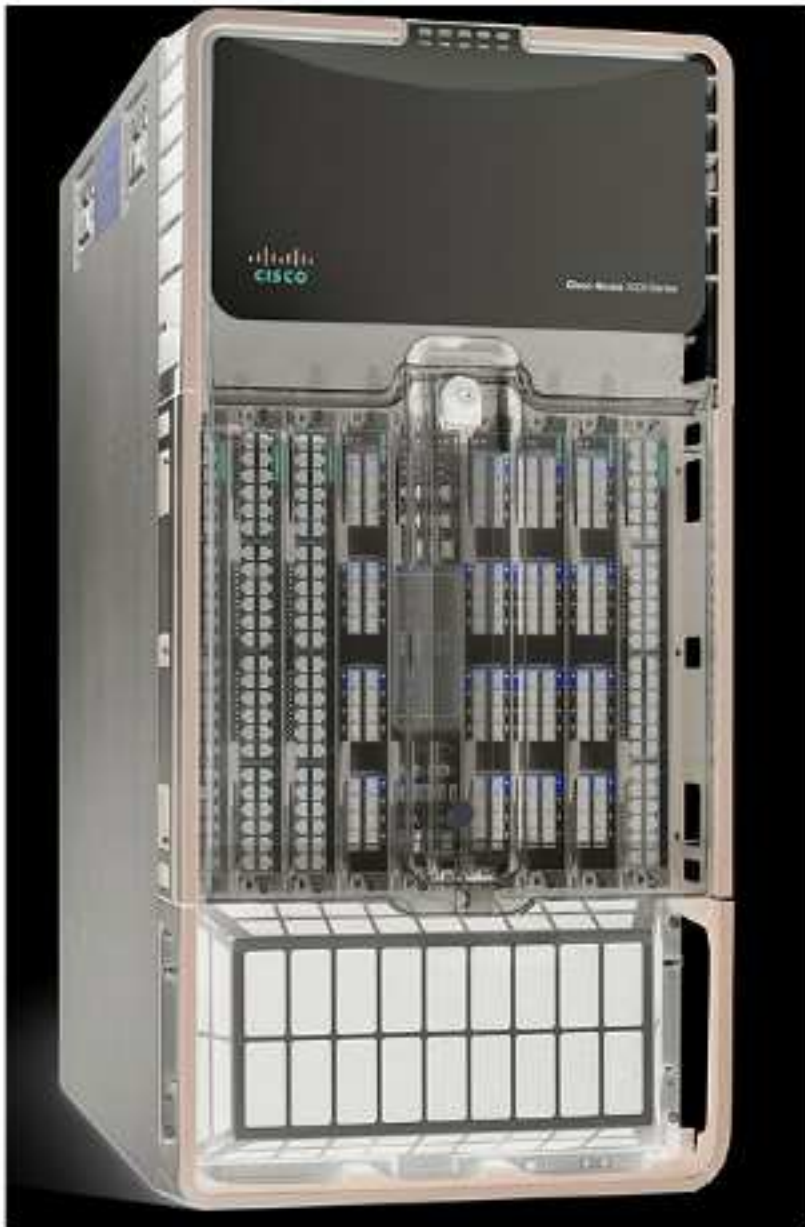
# IP Addresses Are Interfaces



- ◆ All routers have multiple interfaces
- ◆ Many-to-one mapping from interfaces to routers

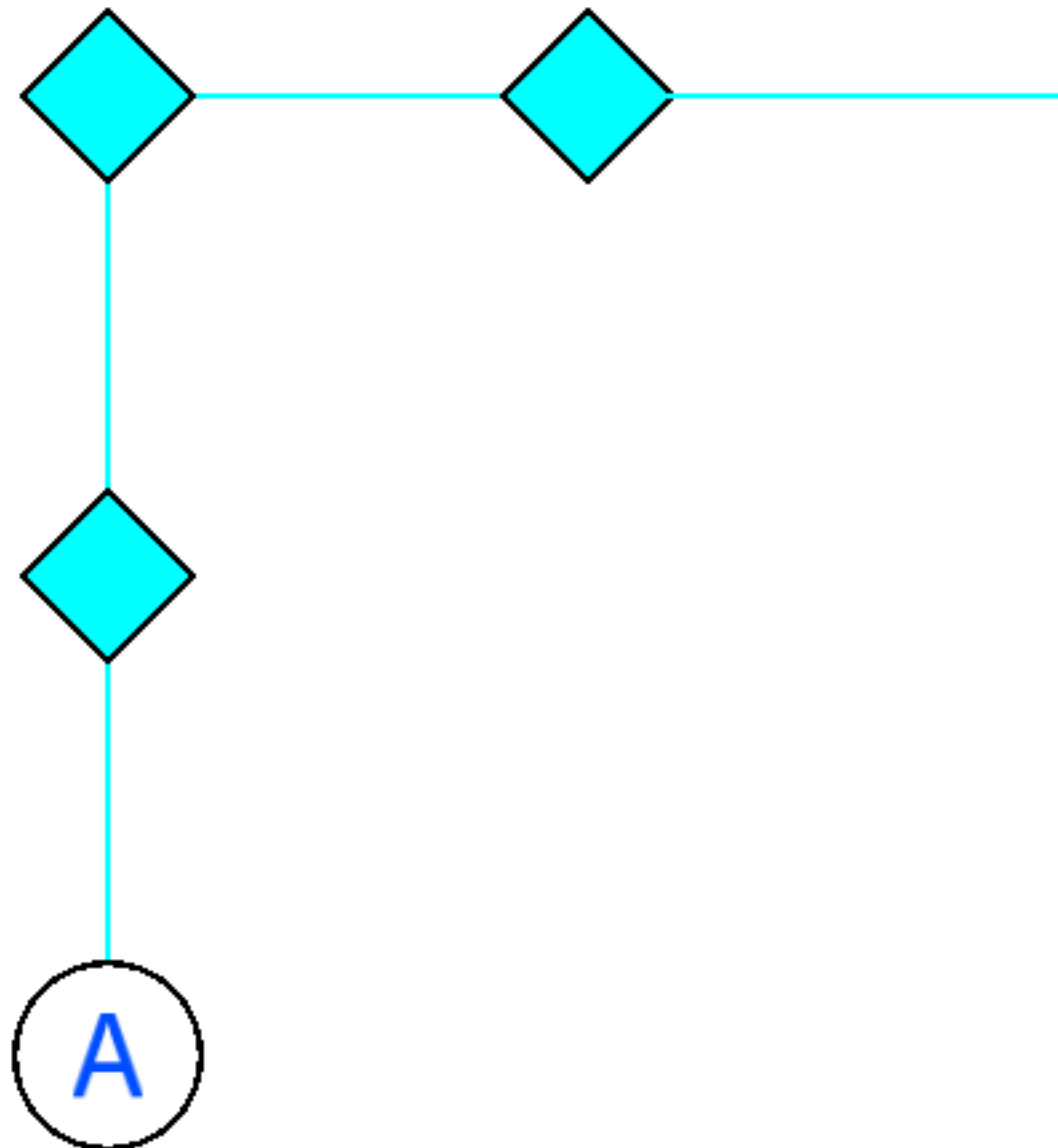


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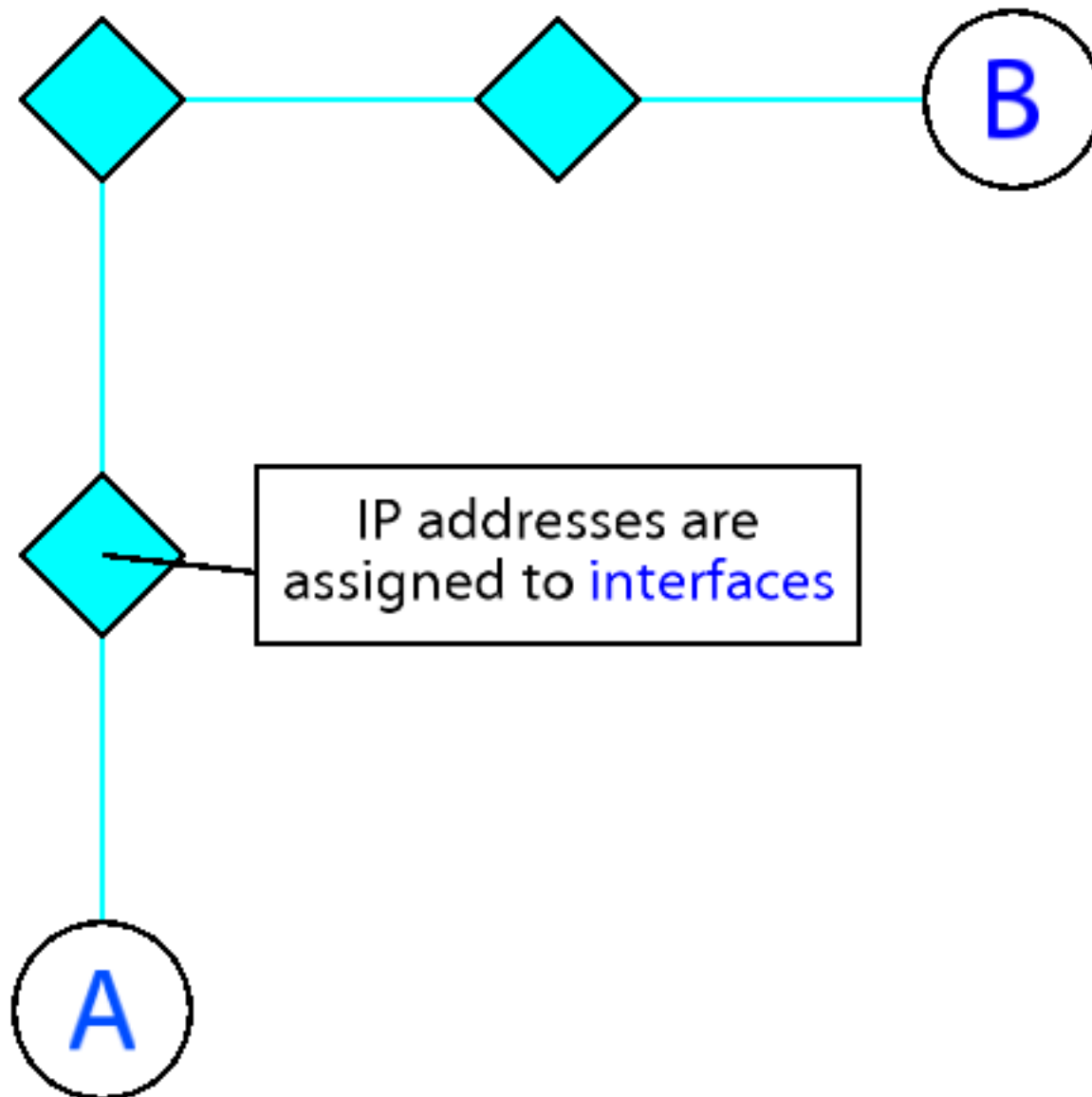


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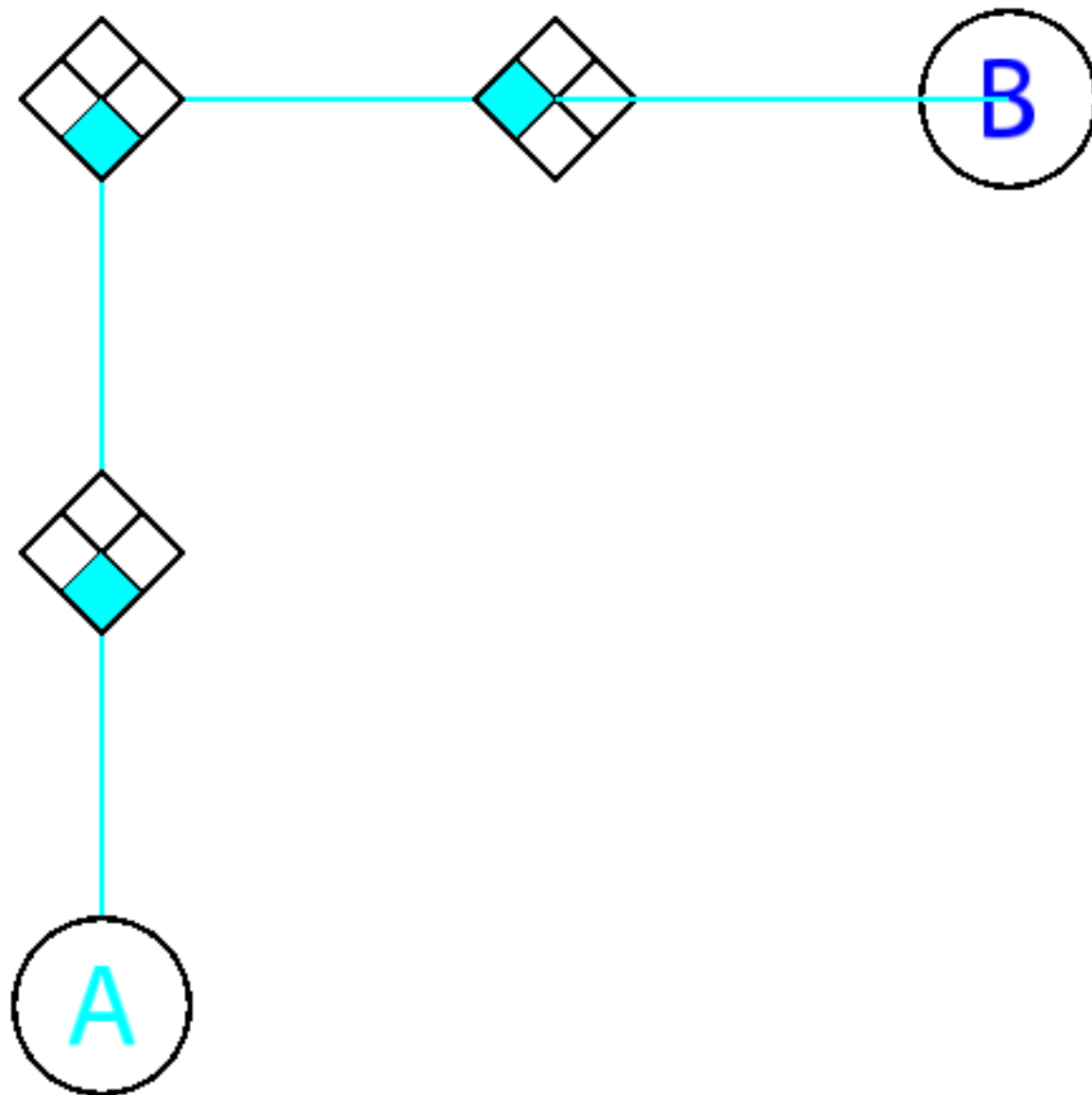
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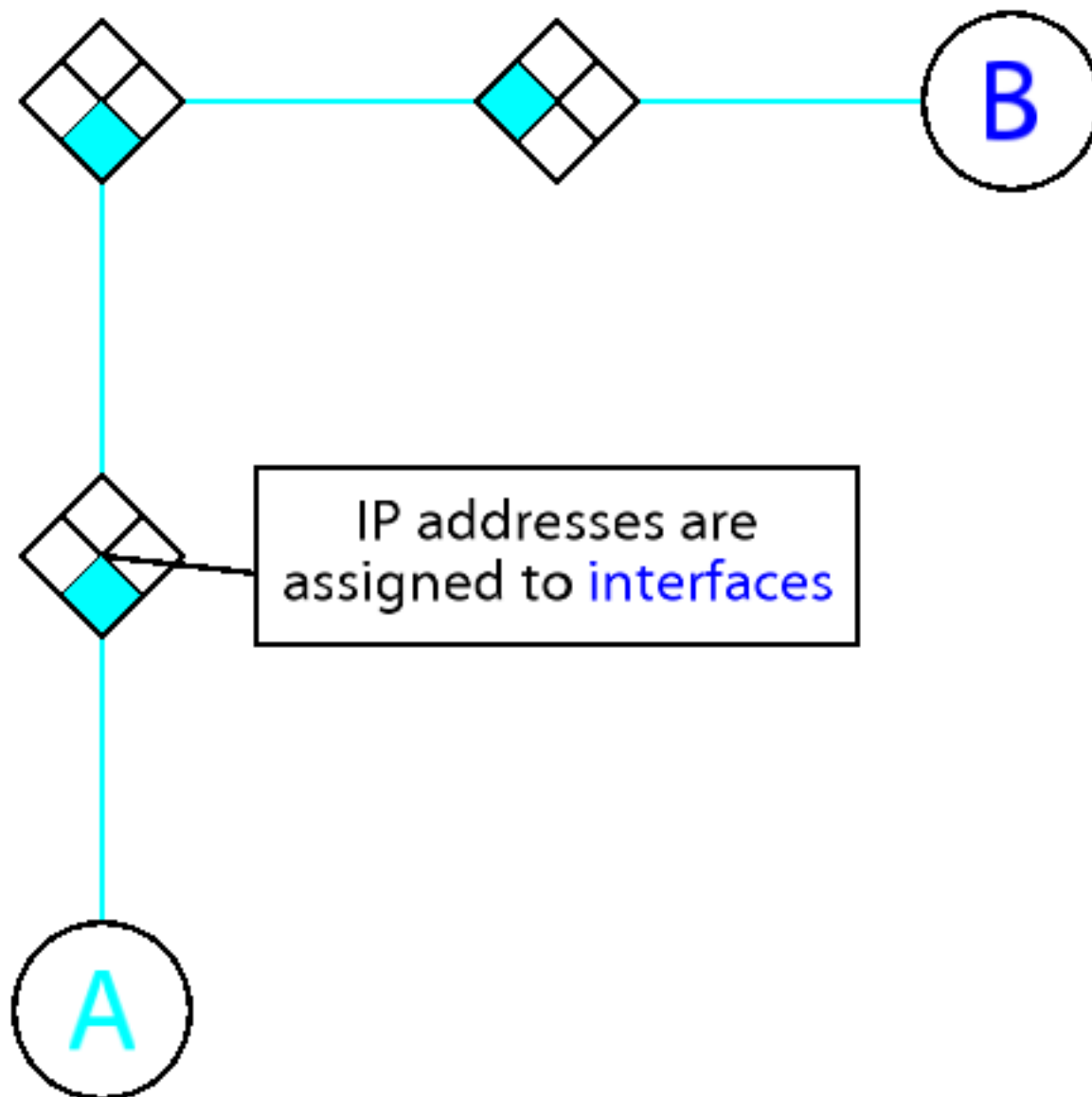
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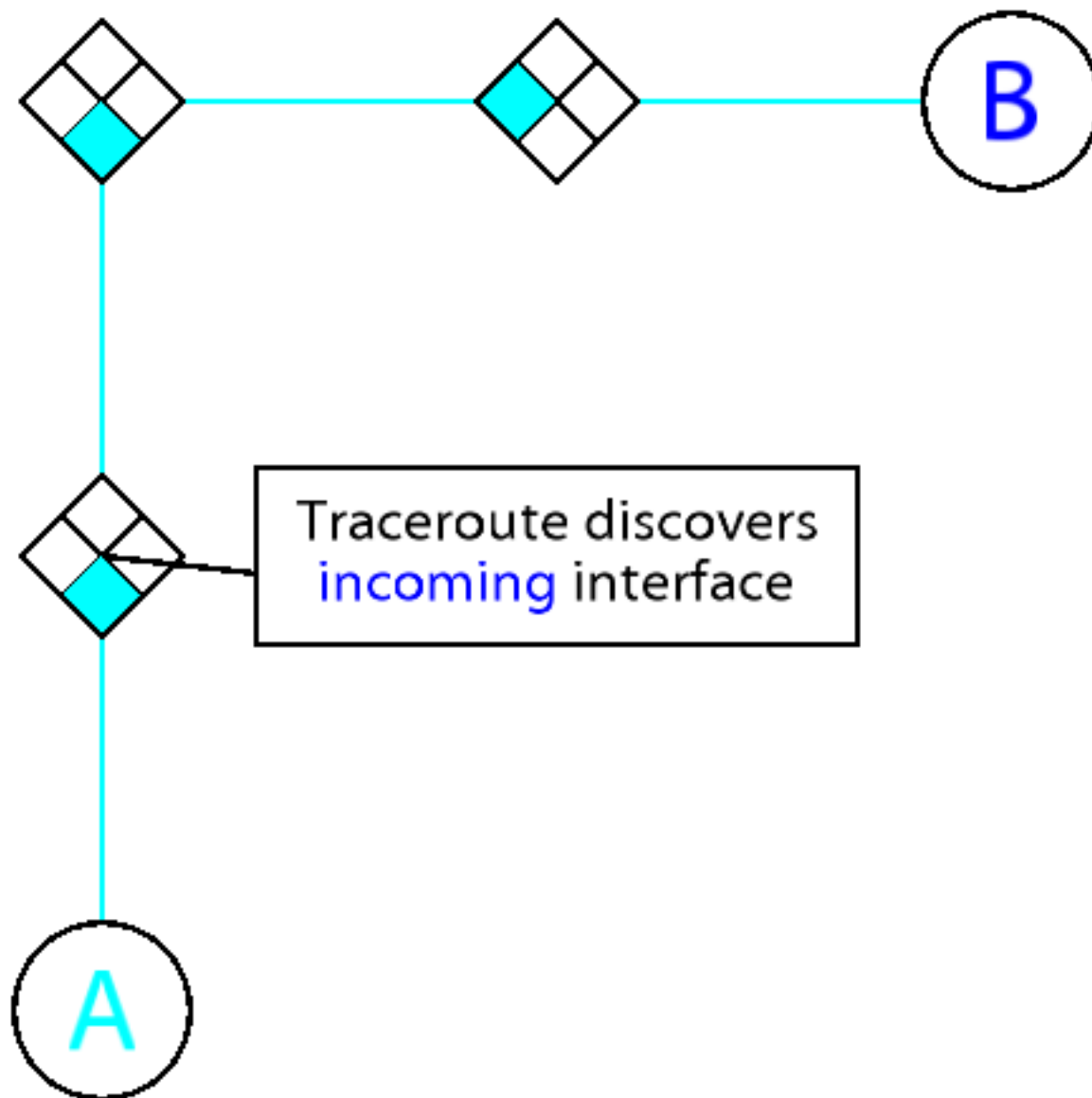
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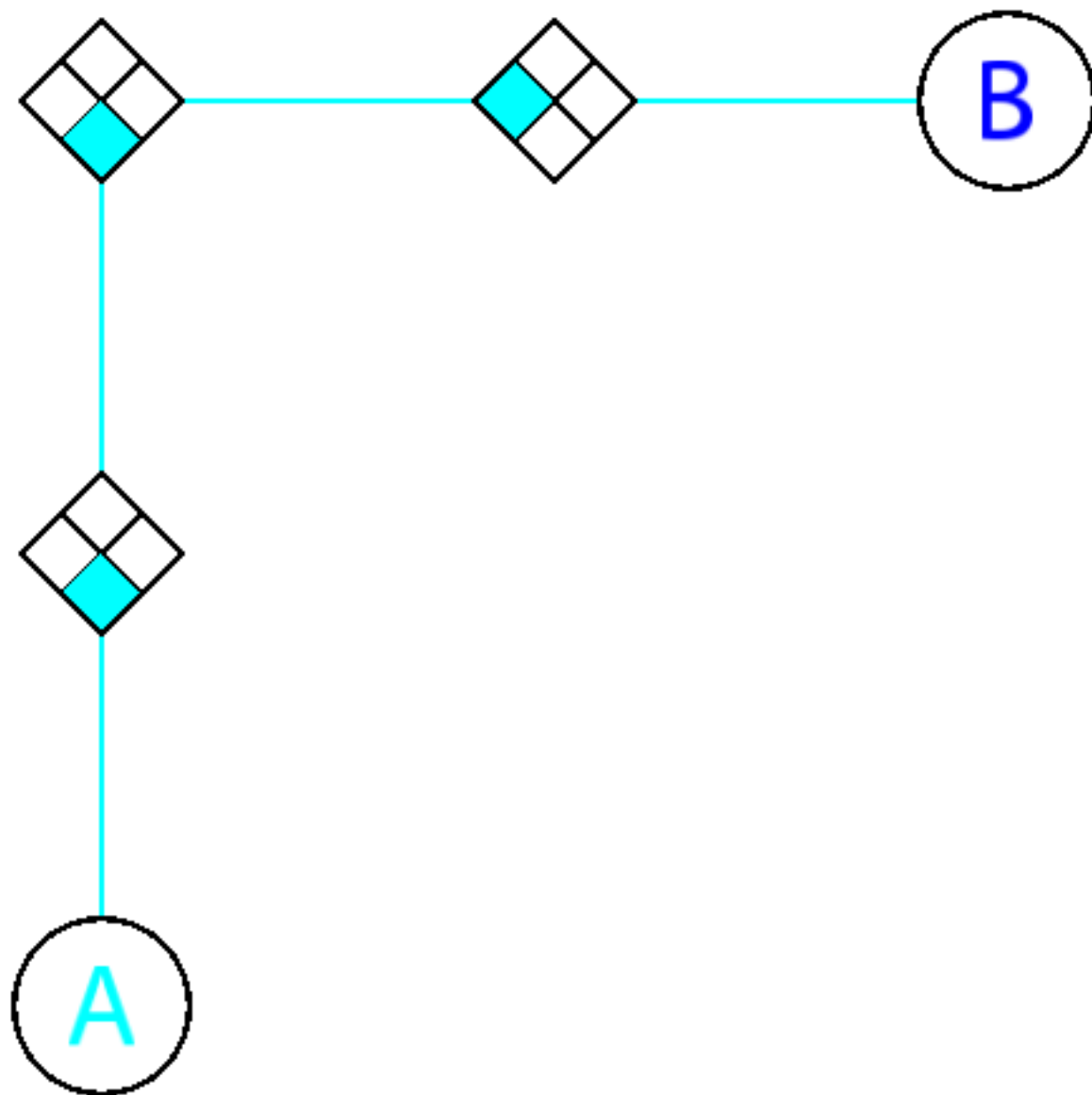
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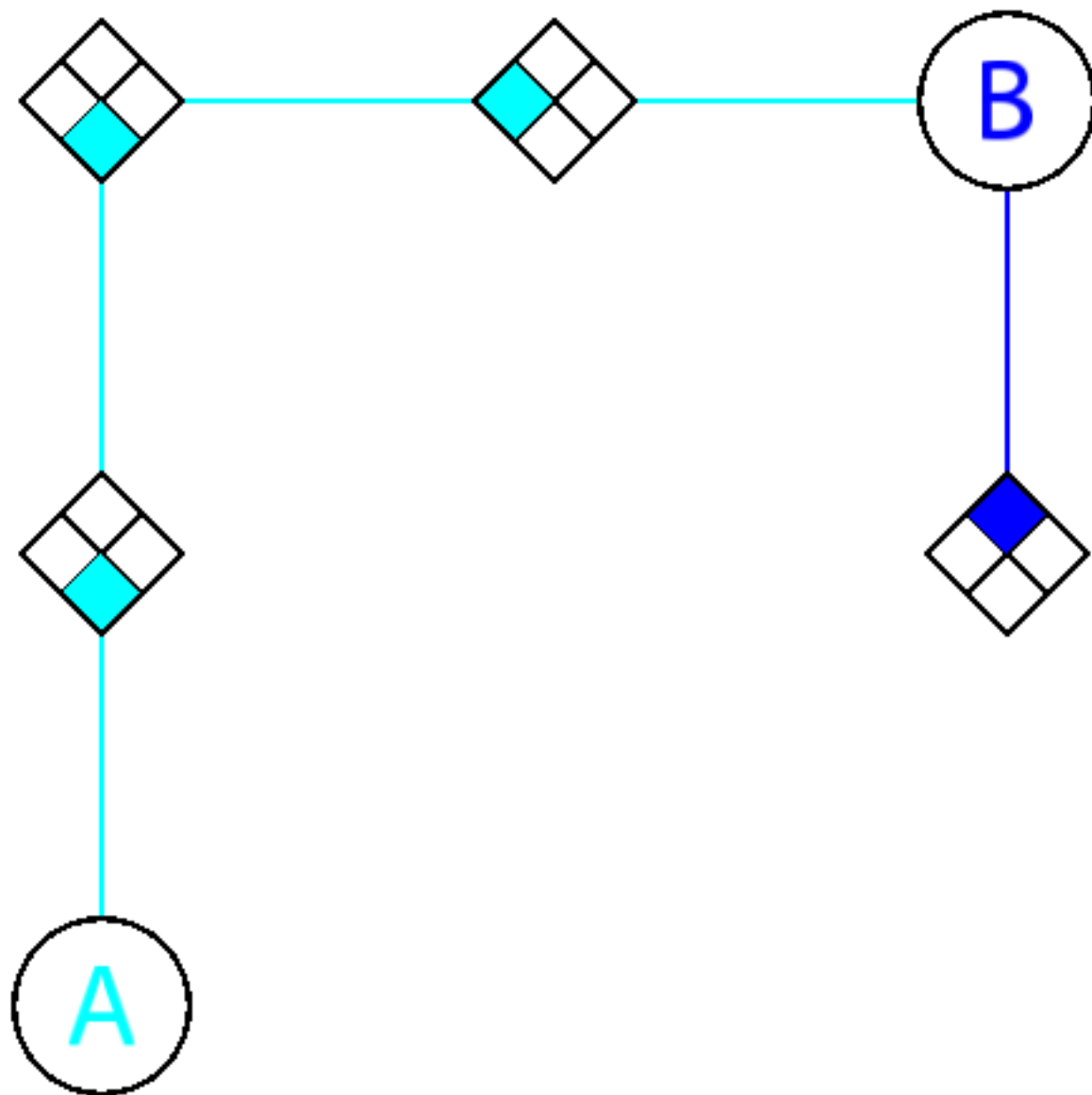
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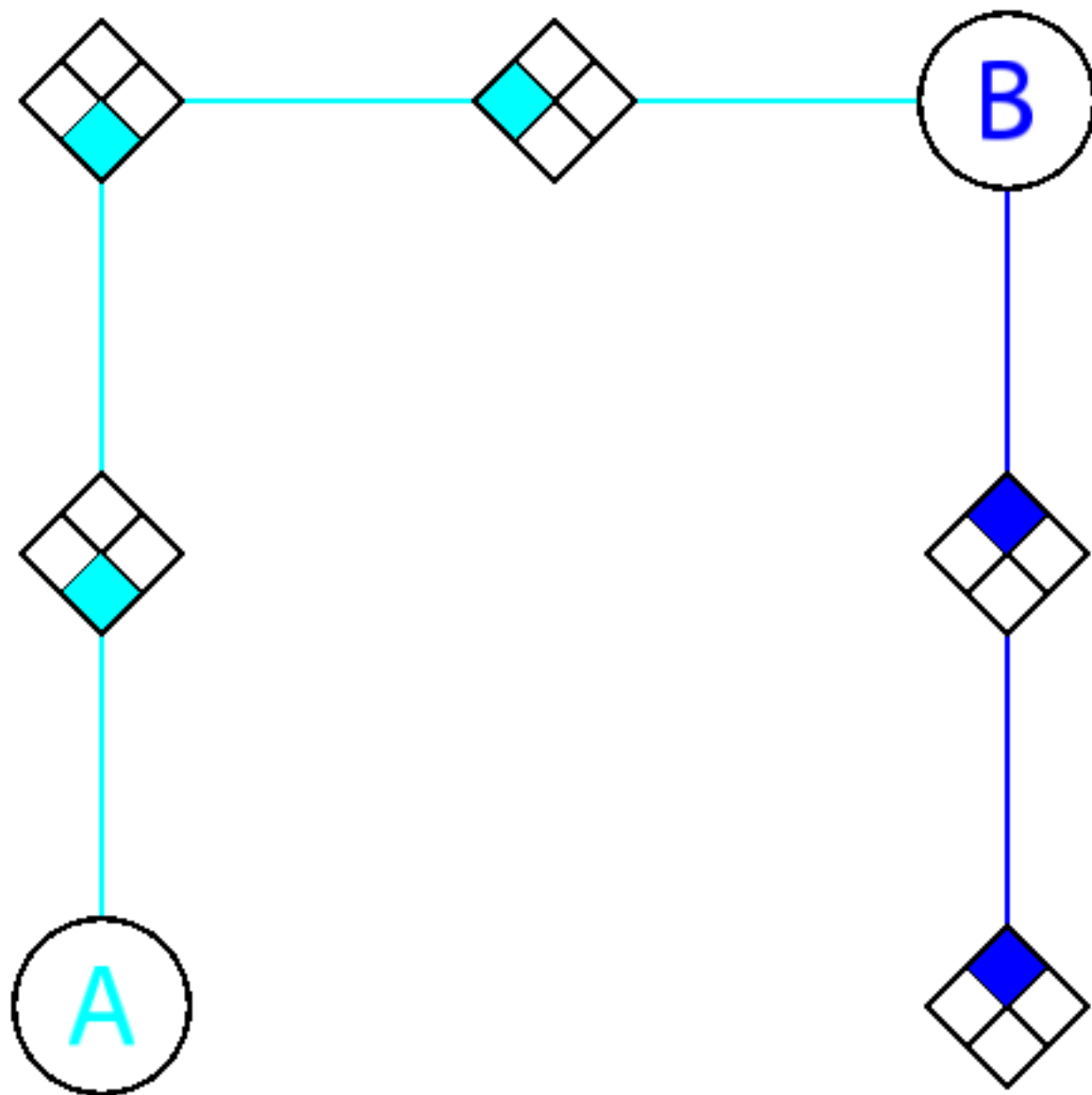


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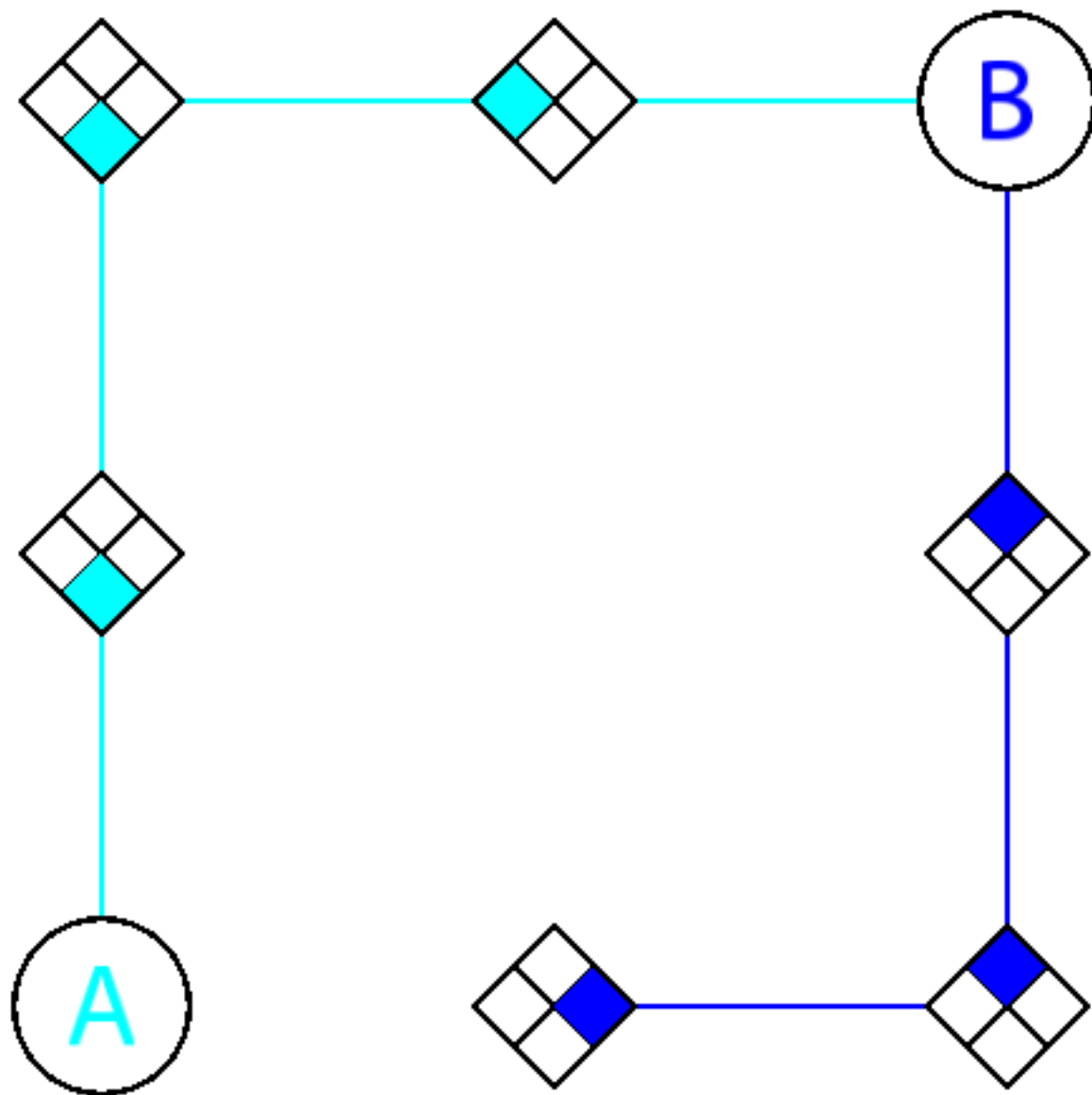




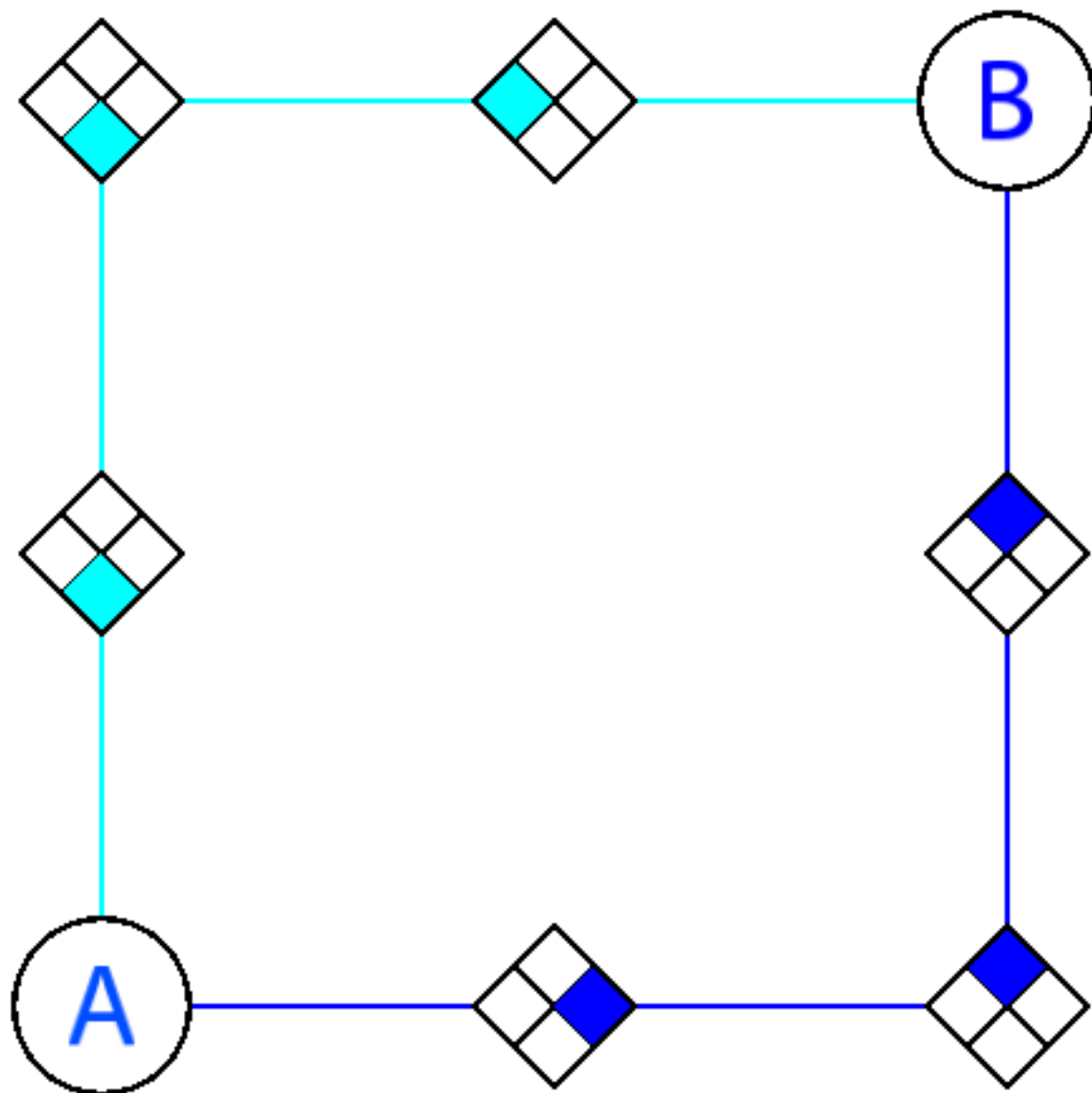
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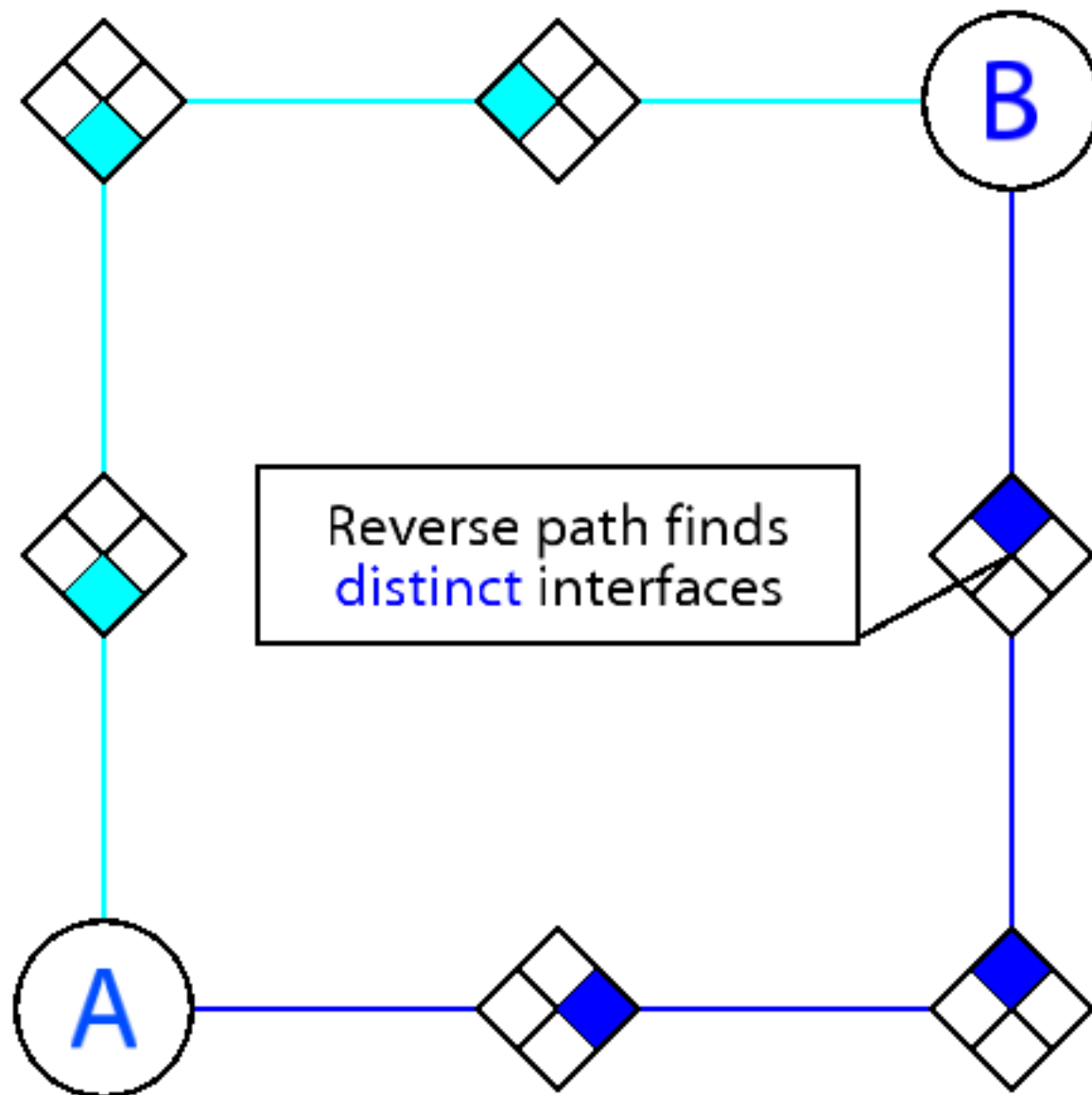
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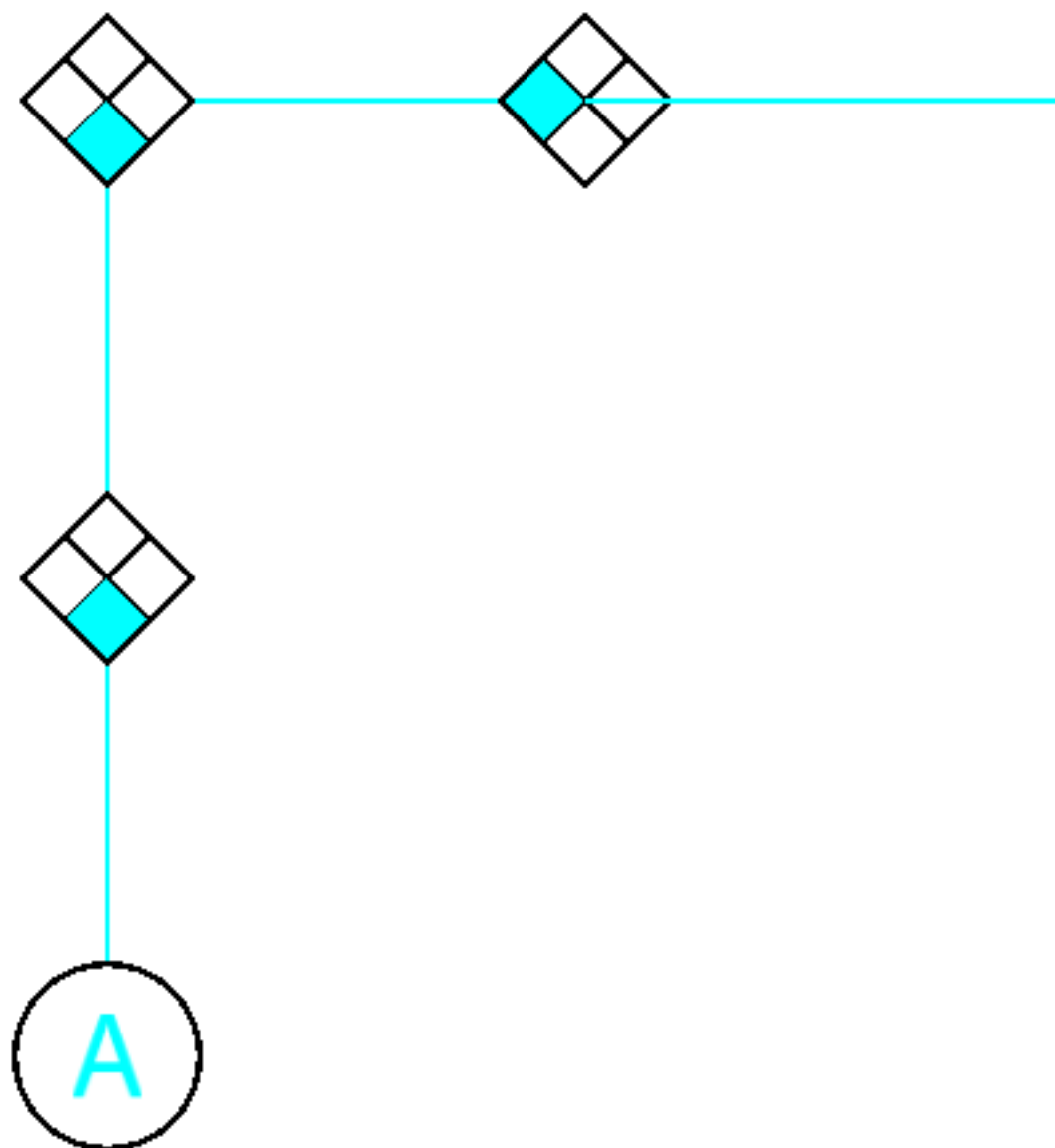
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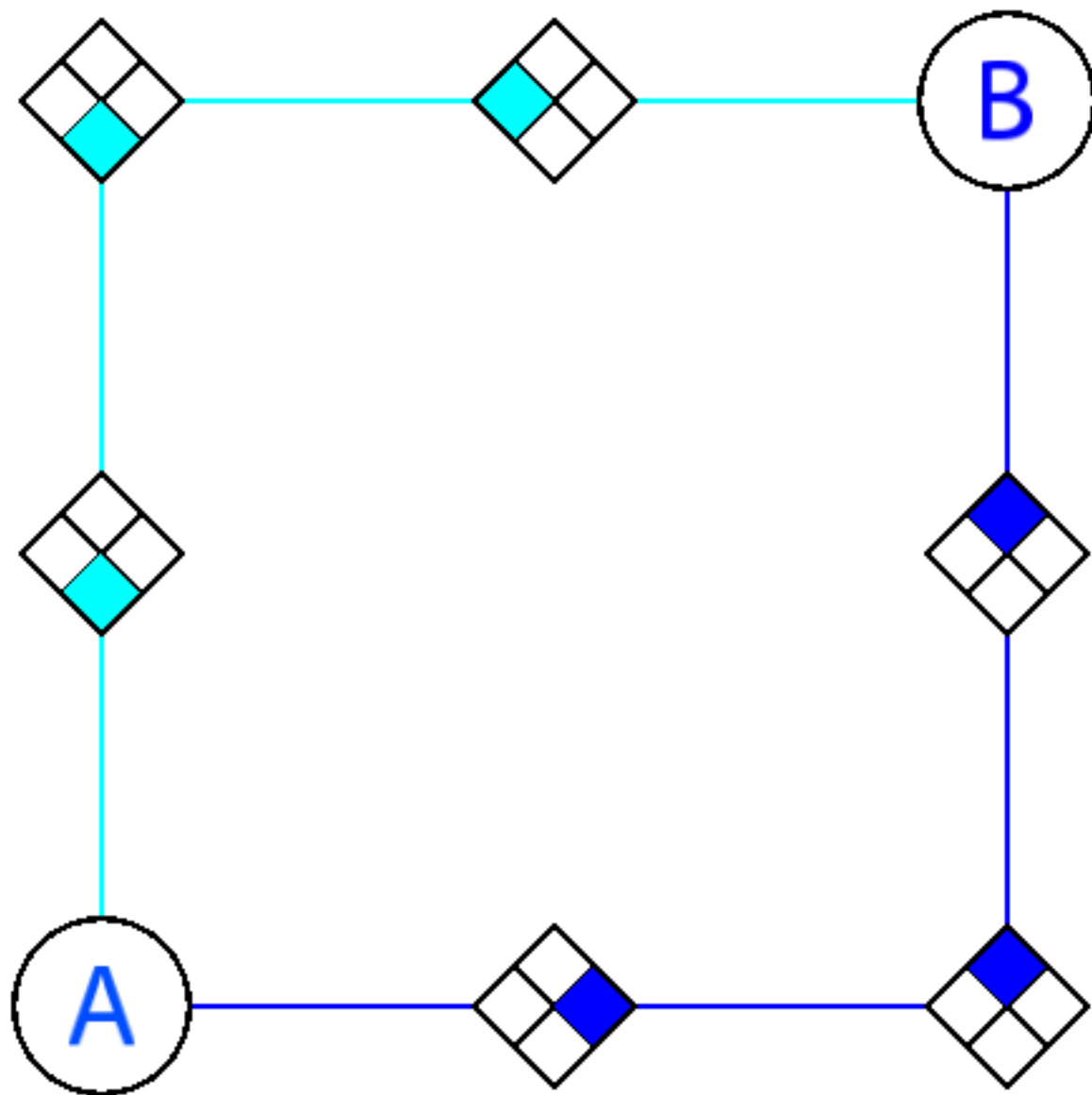
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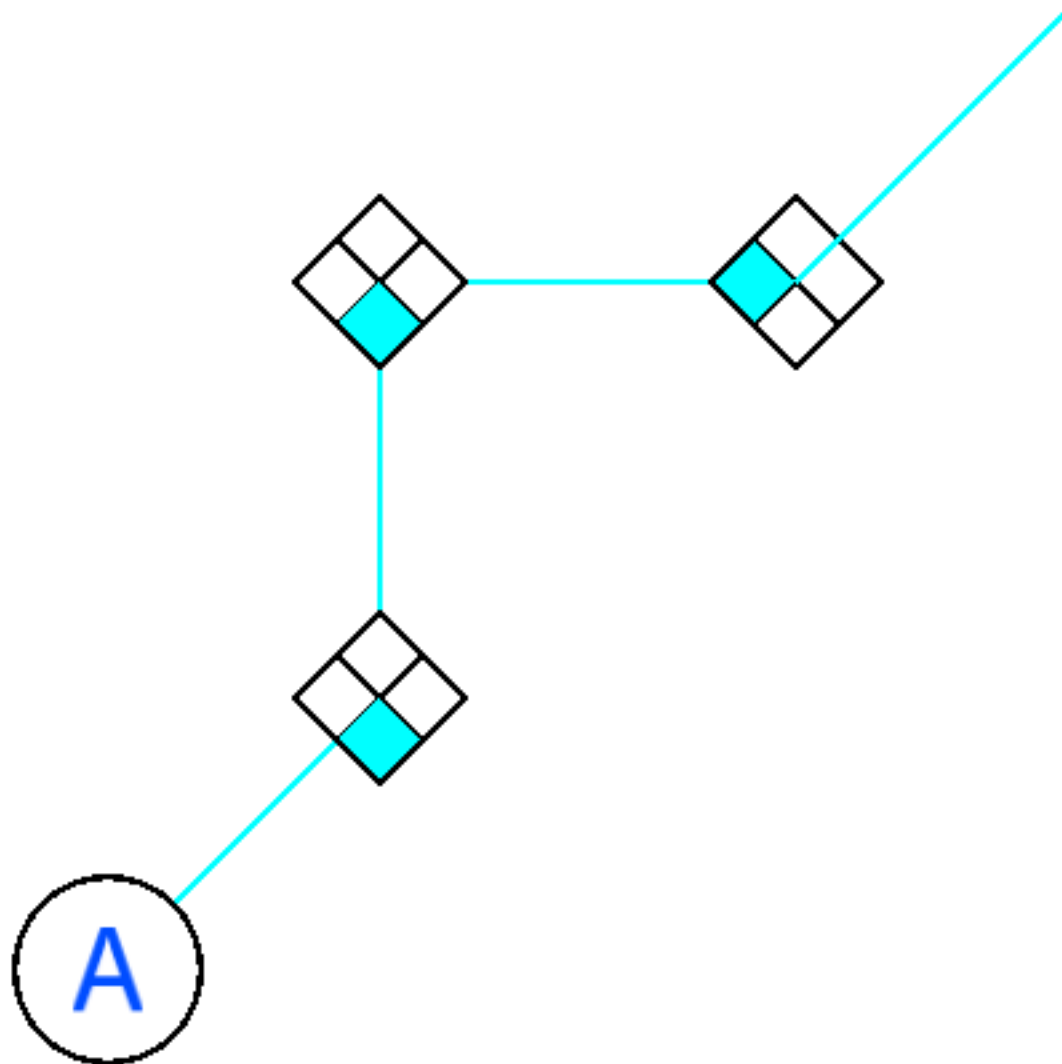
# Ambiguous Topologies



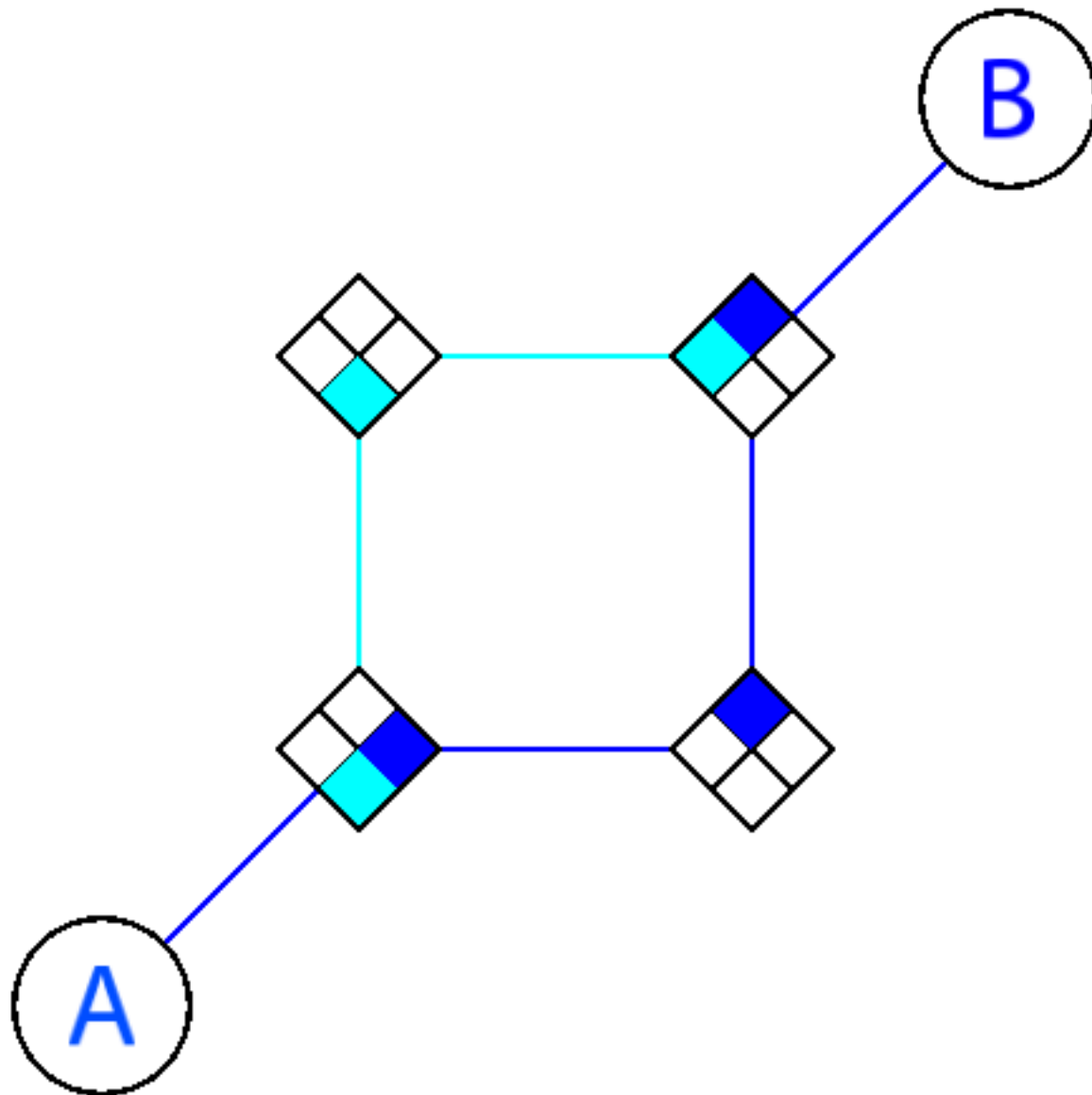
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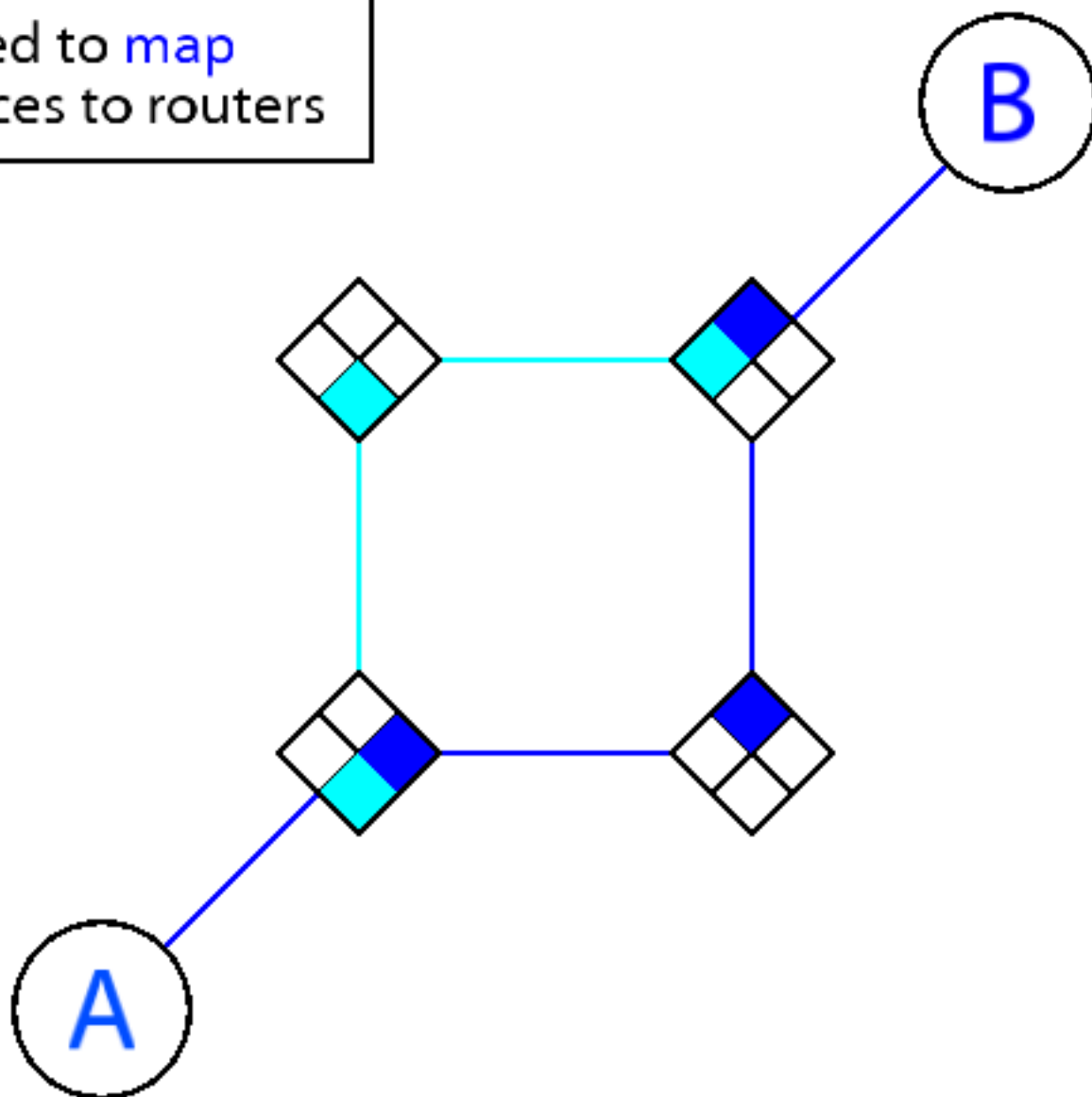
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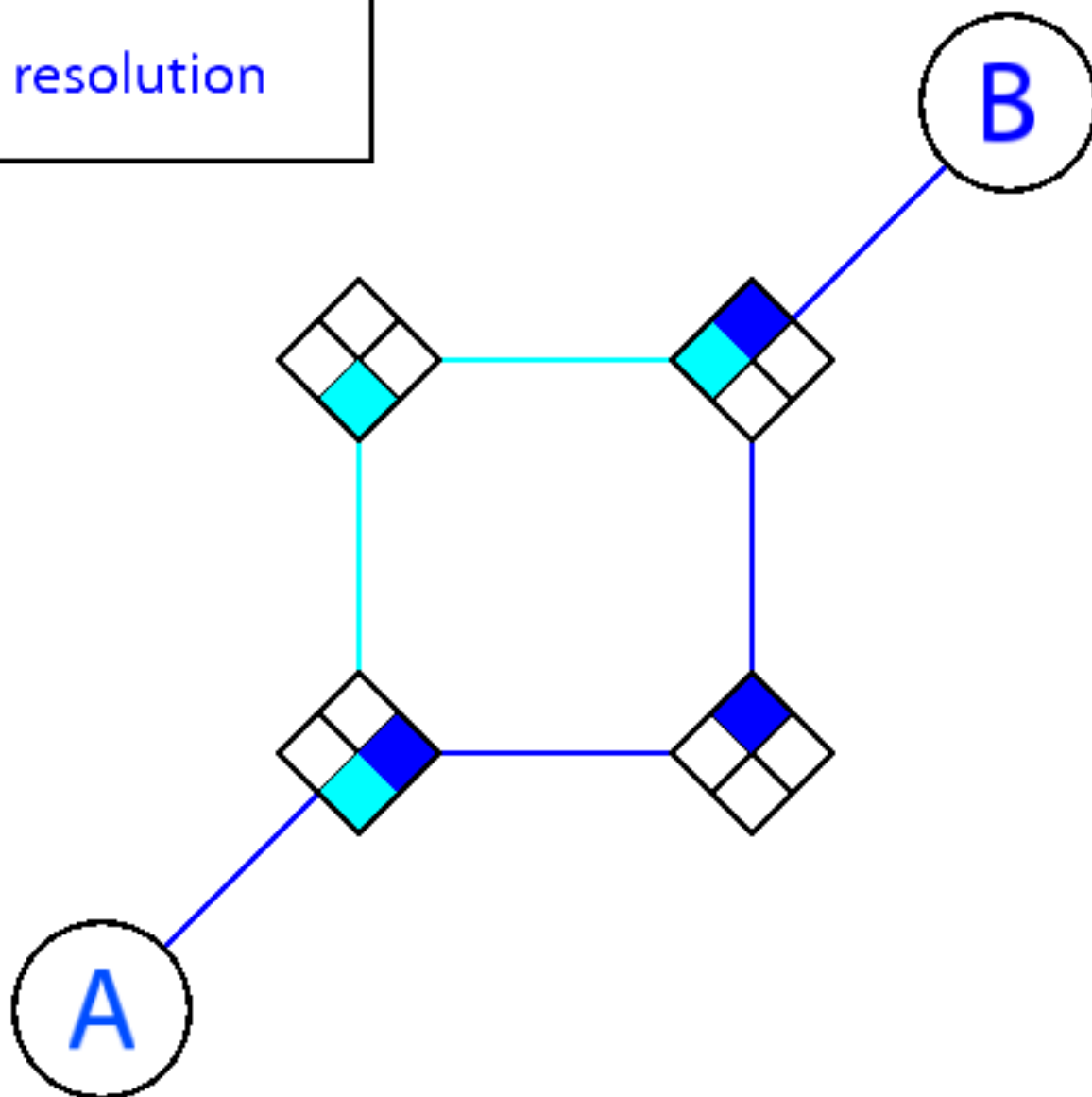
# Ambiguous Topologies

Need to **map**  
interfaces to routers



# Ambiguous Topologies

Alias resolution





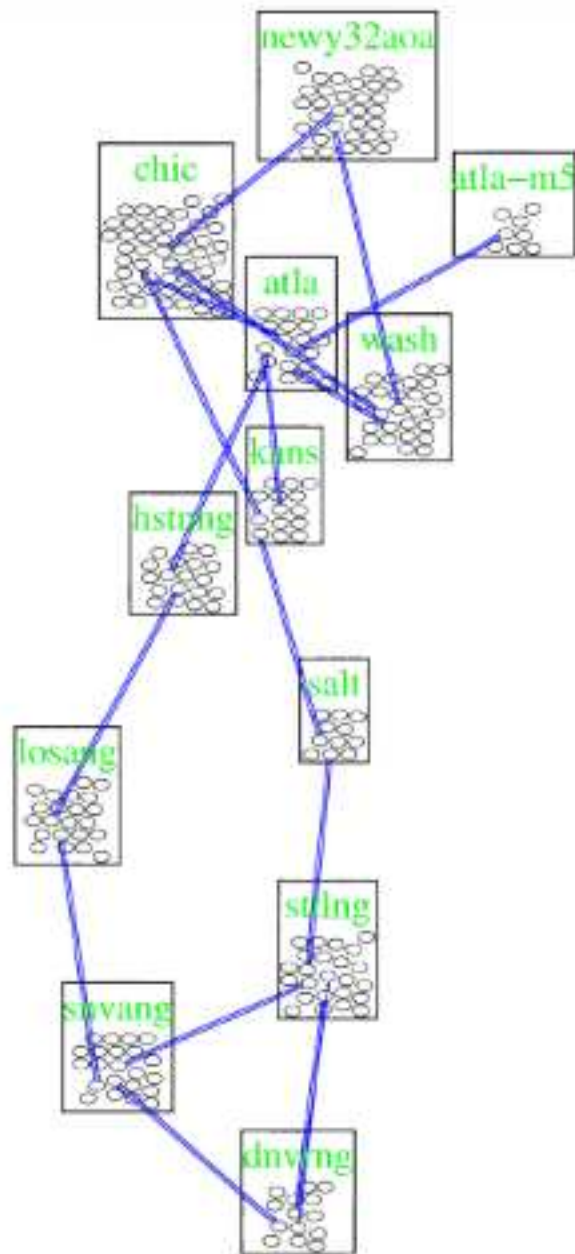
# Alias Resolution

- ◆ Direct probing techniques
  - Source IP matching: [Mercator](#)
  - IP ID matching: [Rocketfuel](#)
  - 32% of IP addresses are unresponsive
- ◆ Indirect resolution techniques are incomplete and have false positives
- ◆ Bad/missing aliases affect mapping

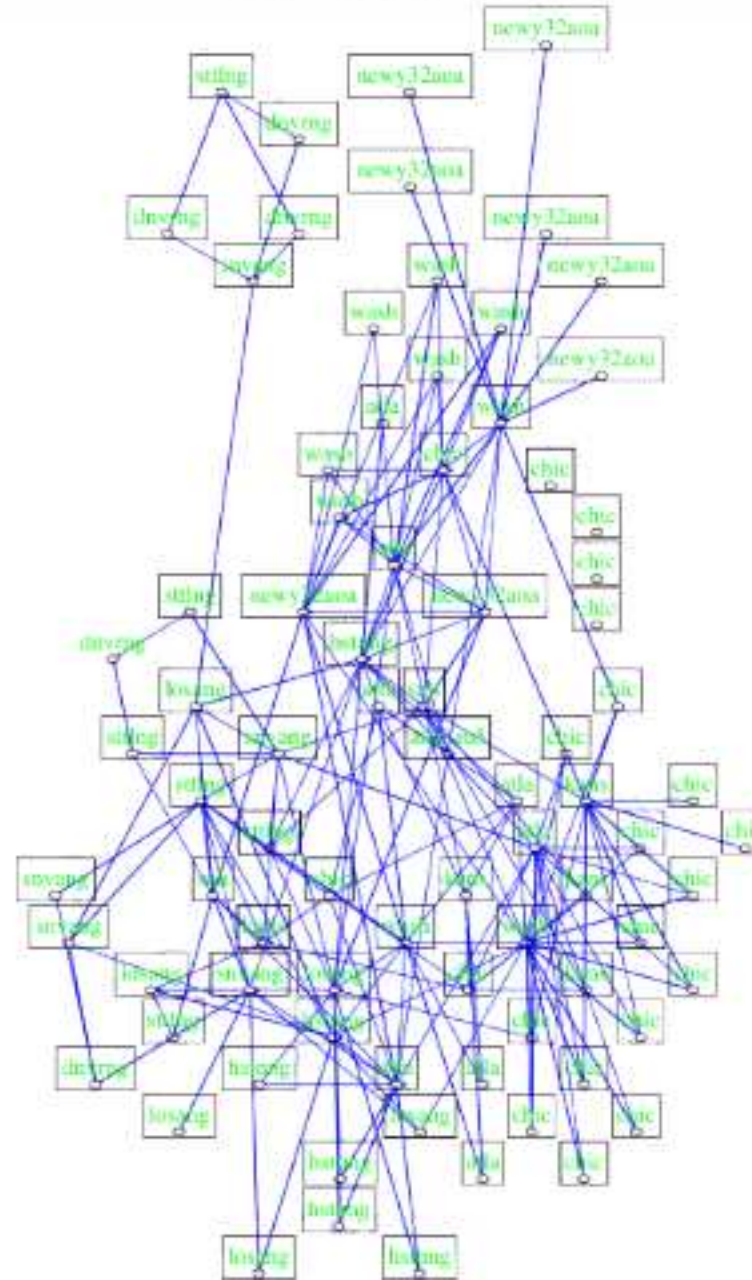


# The Importance of Aliases

**Actual**

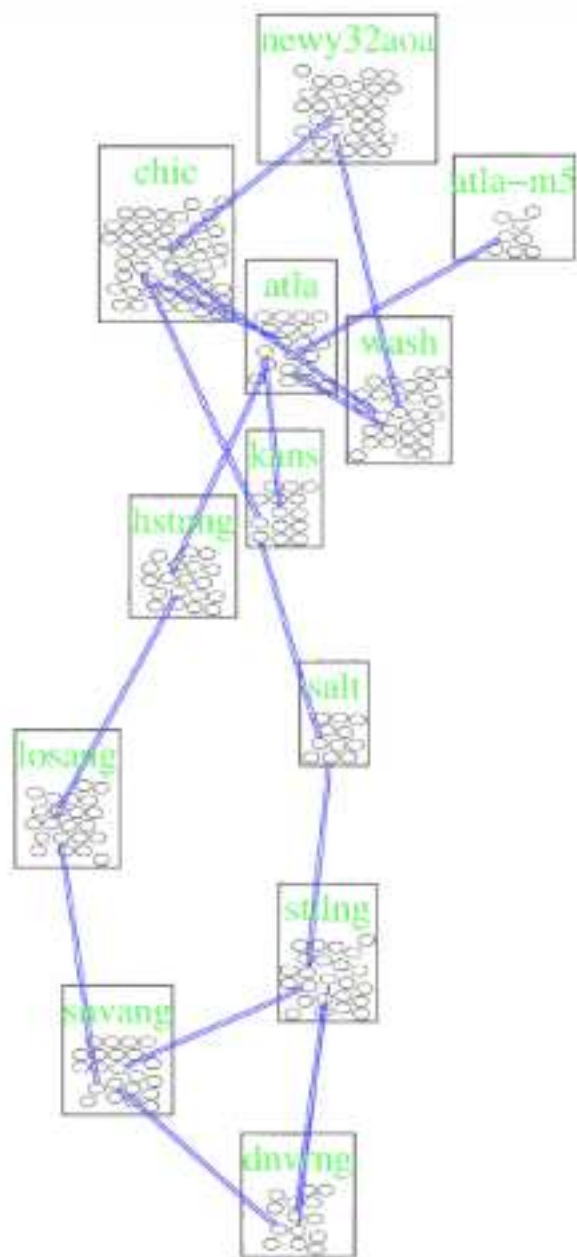


**Rocketfuel**

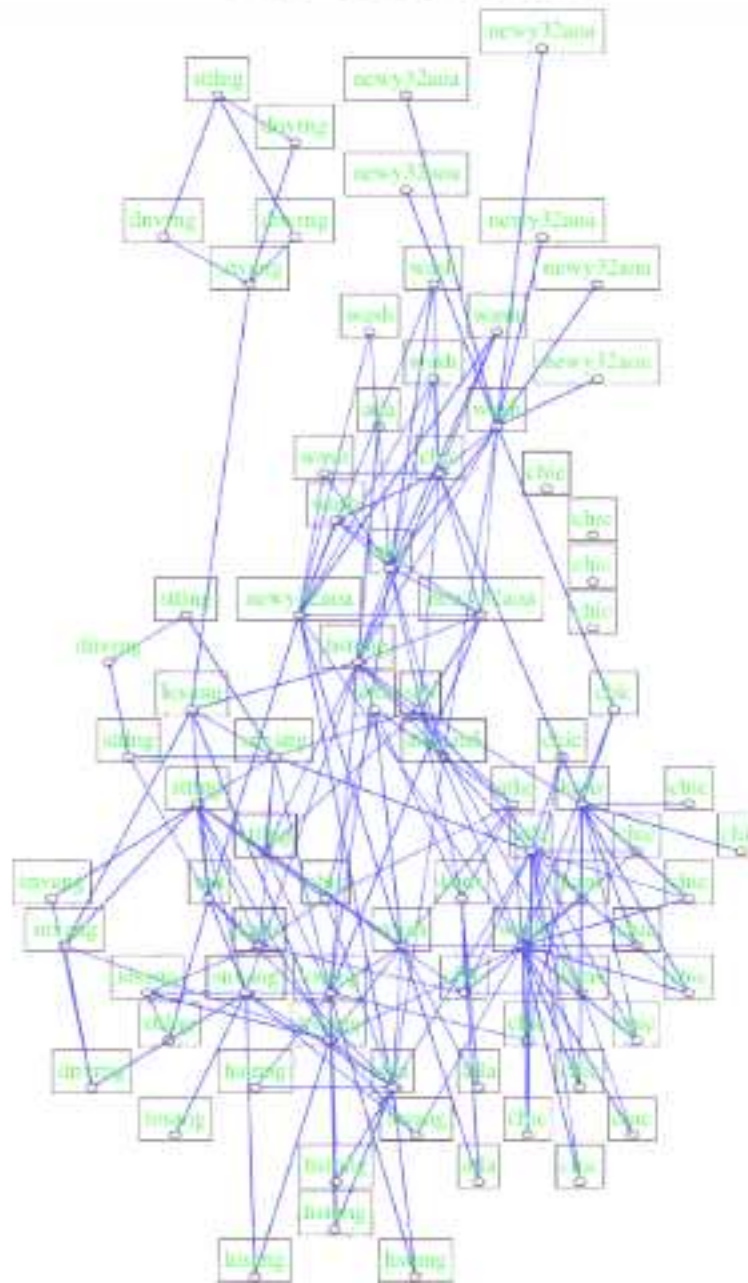


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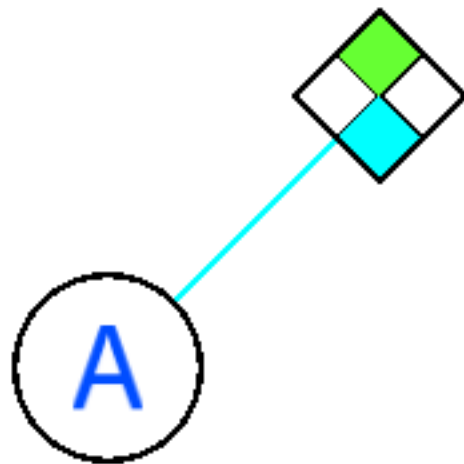


# Record Route Finds Aliases

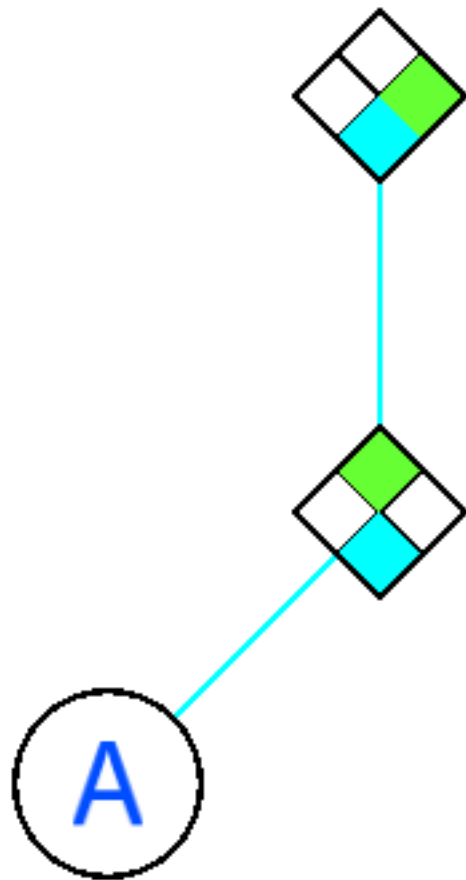




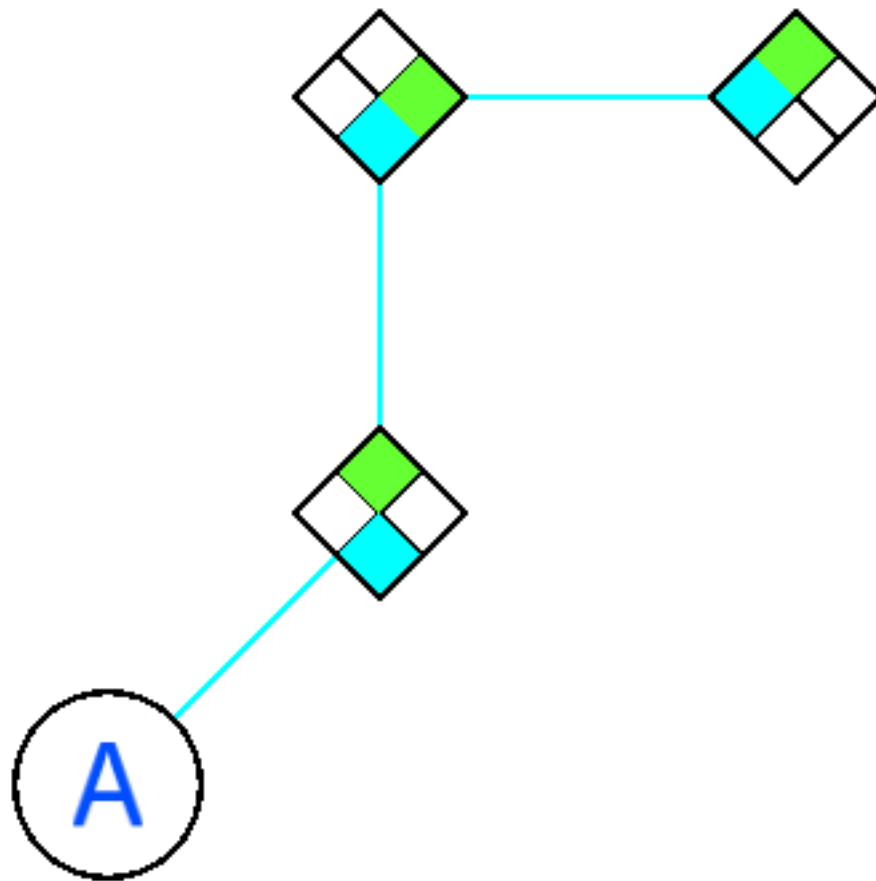
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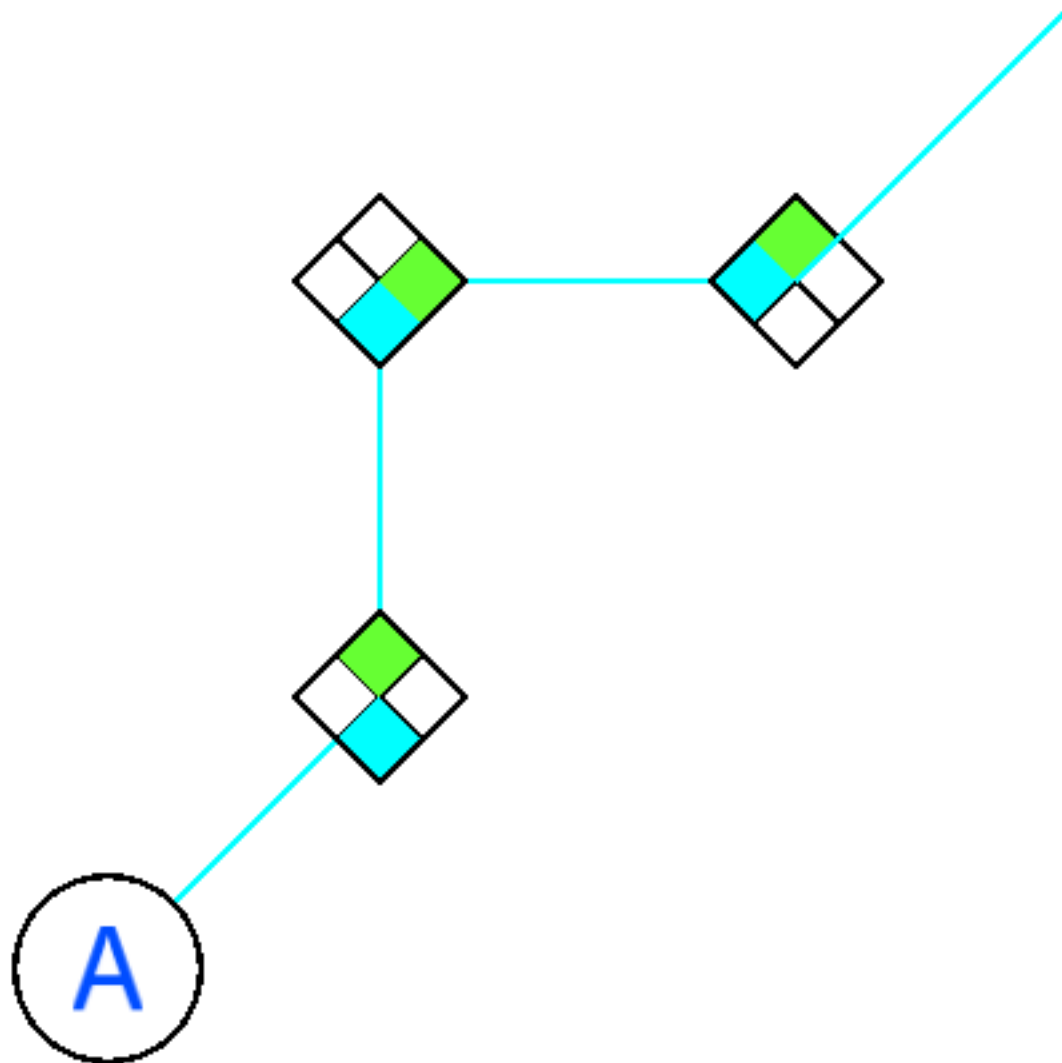
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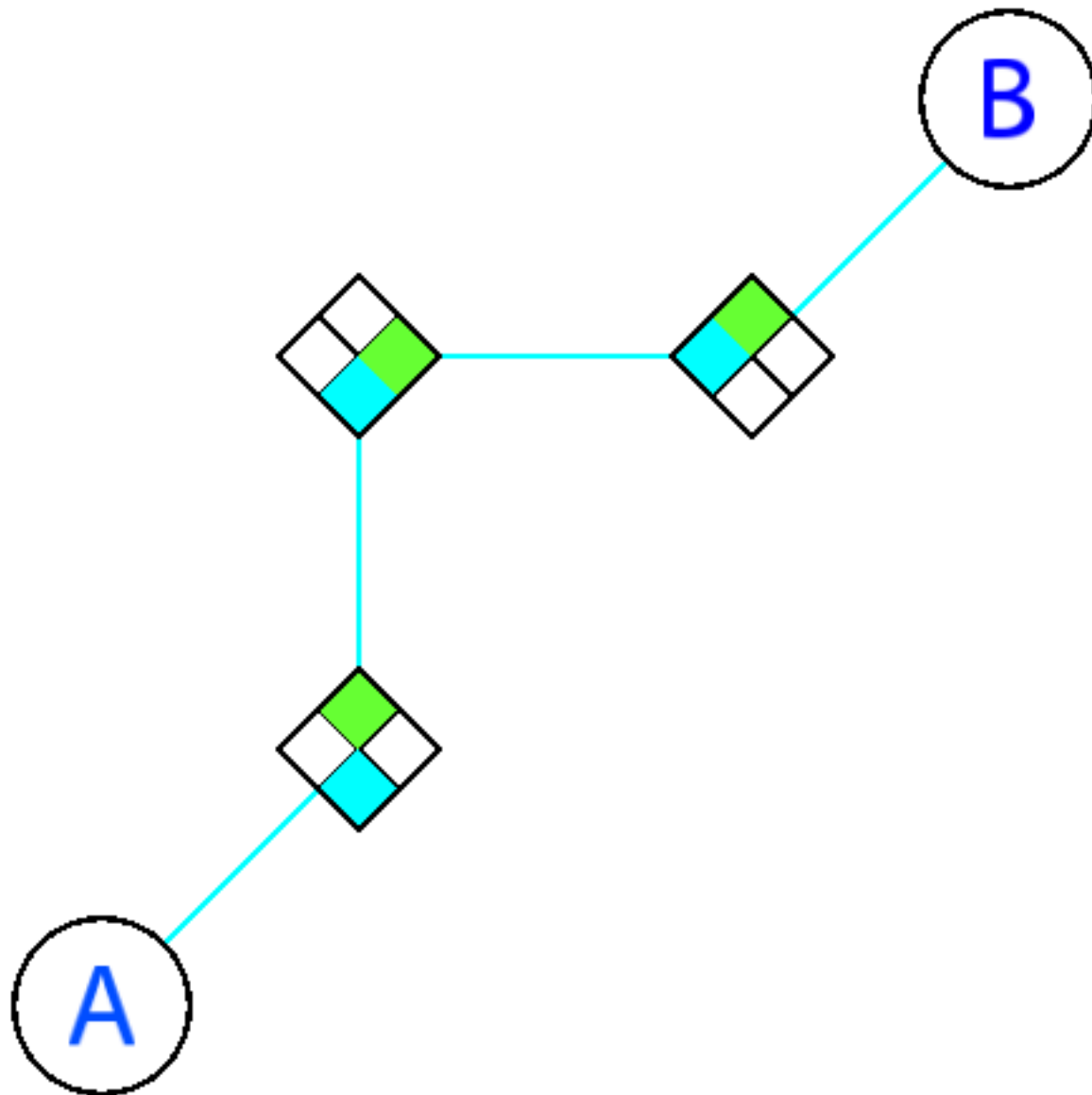
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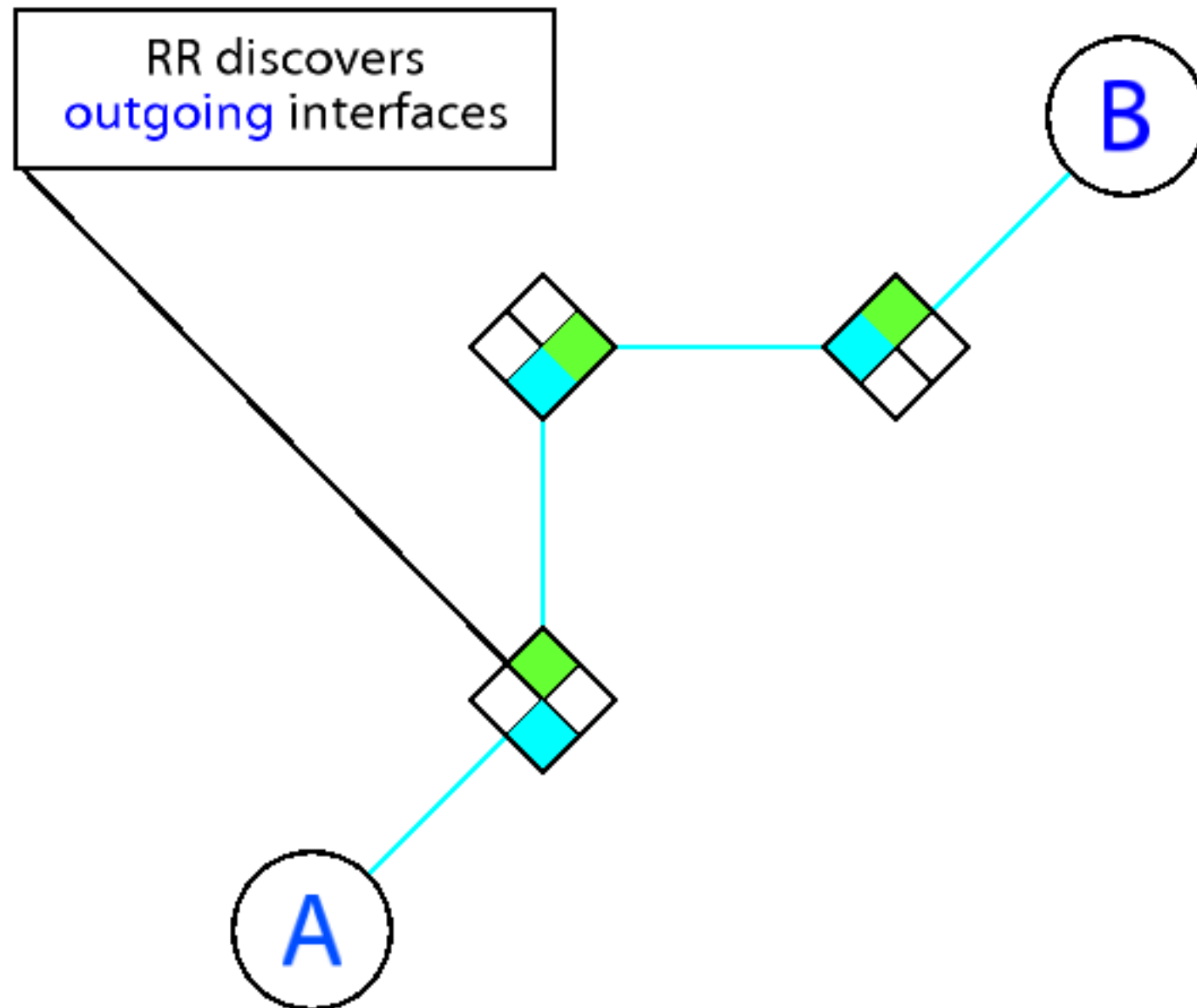
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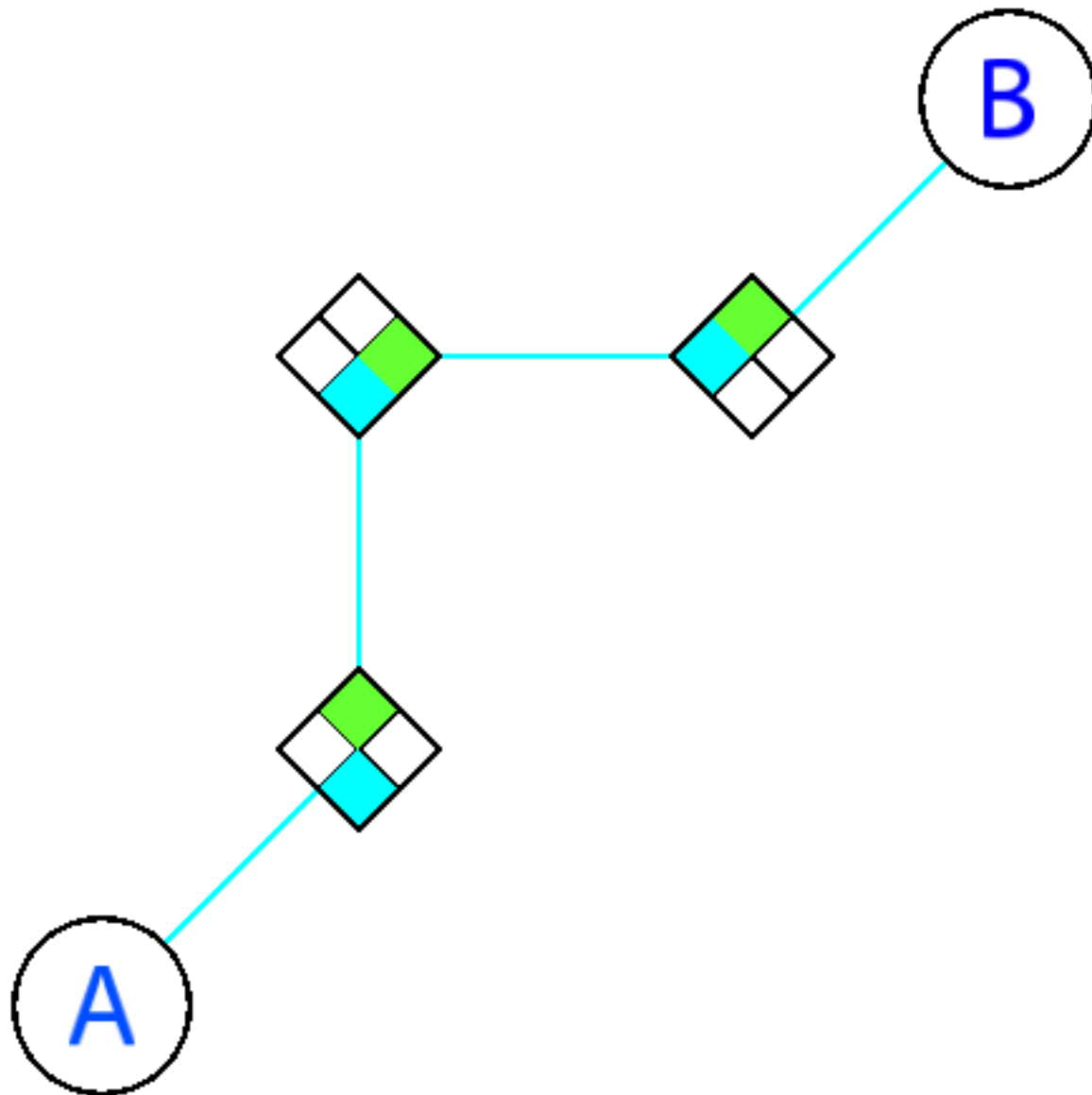
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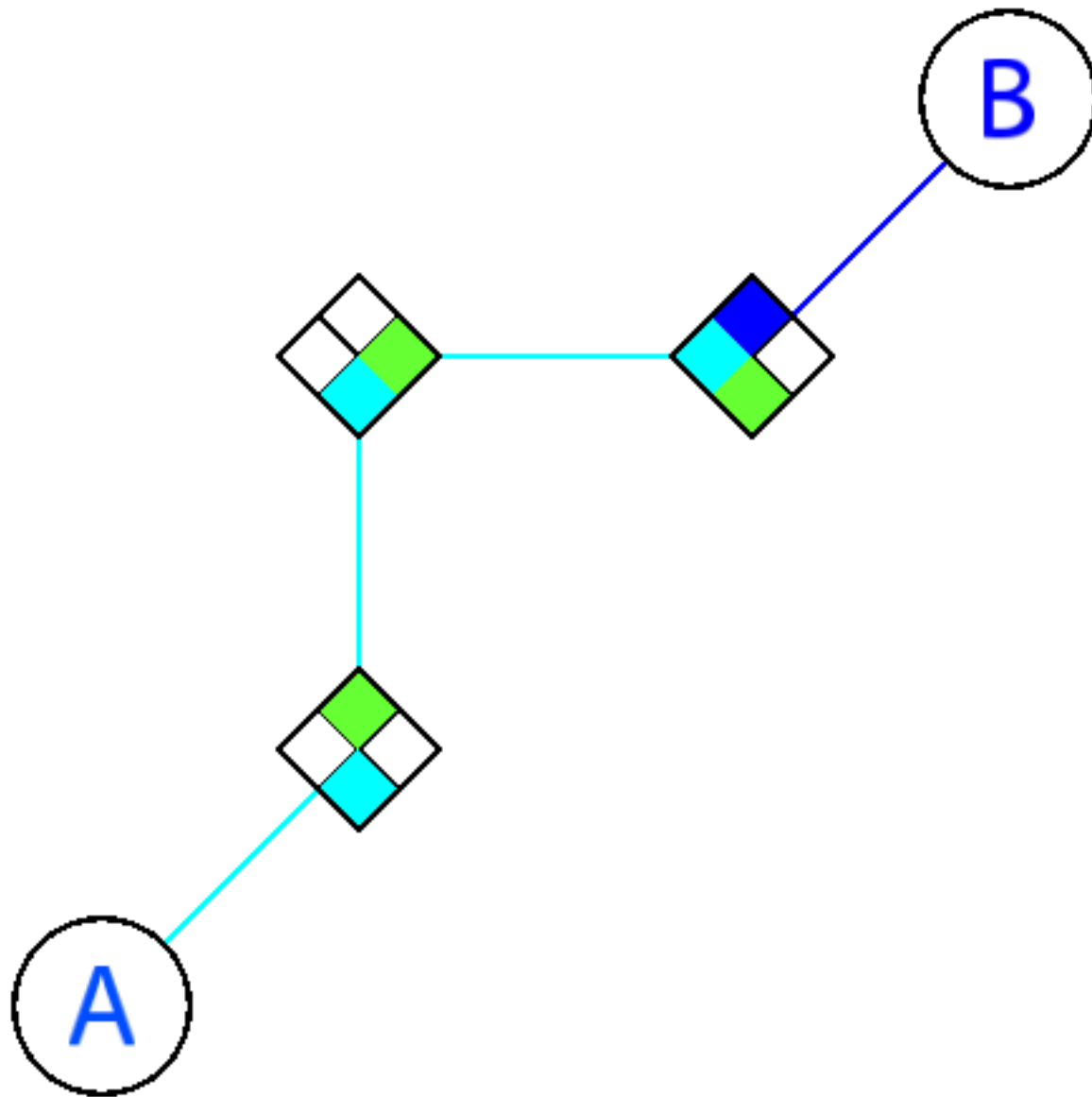
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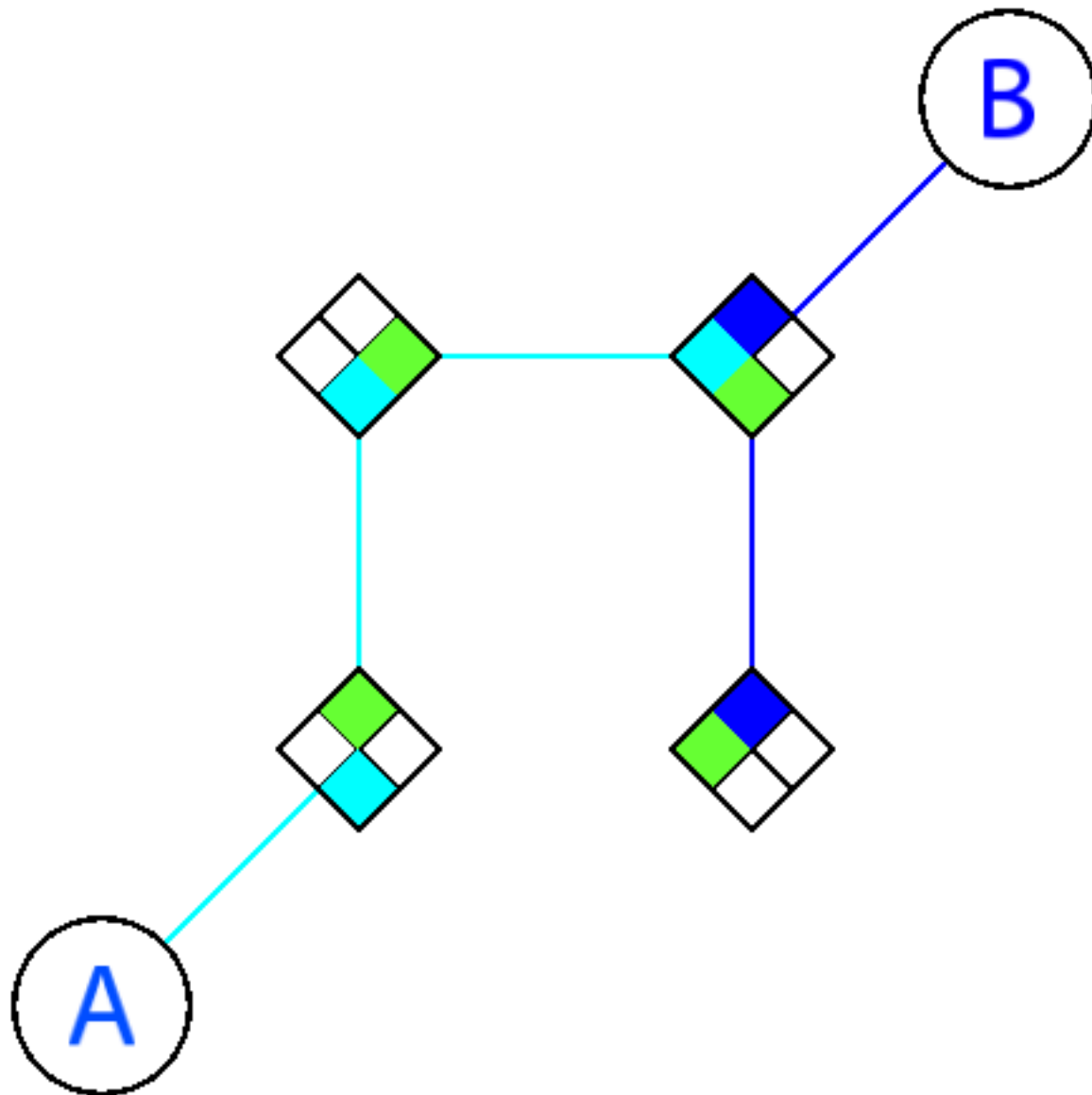


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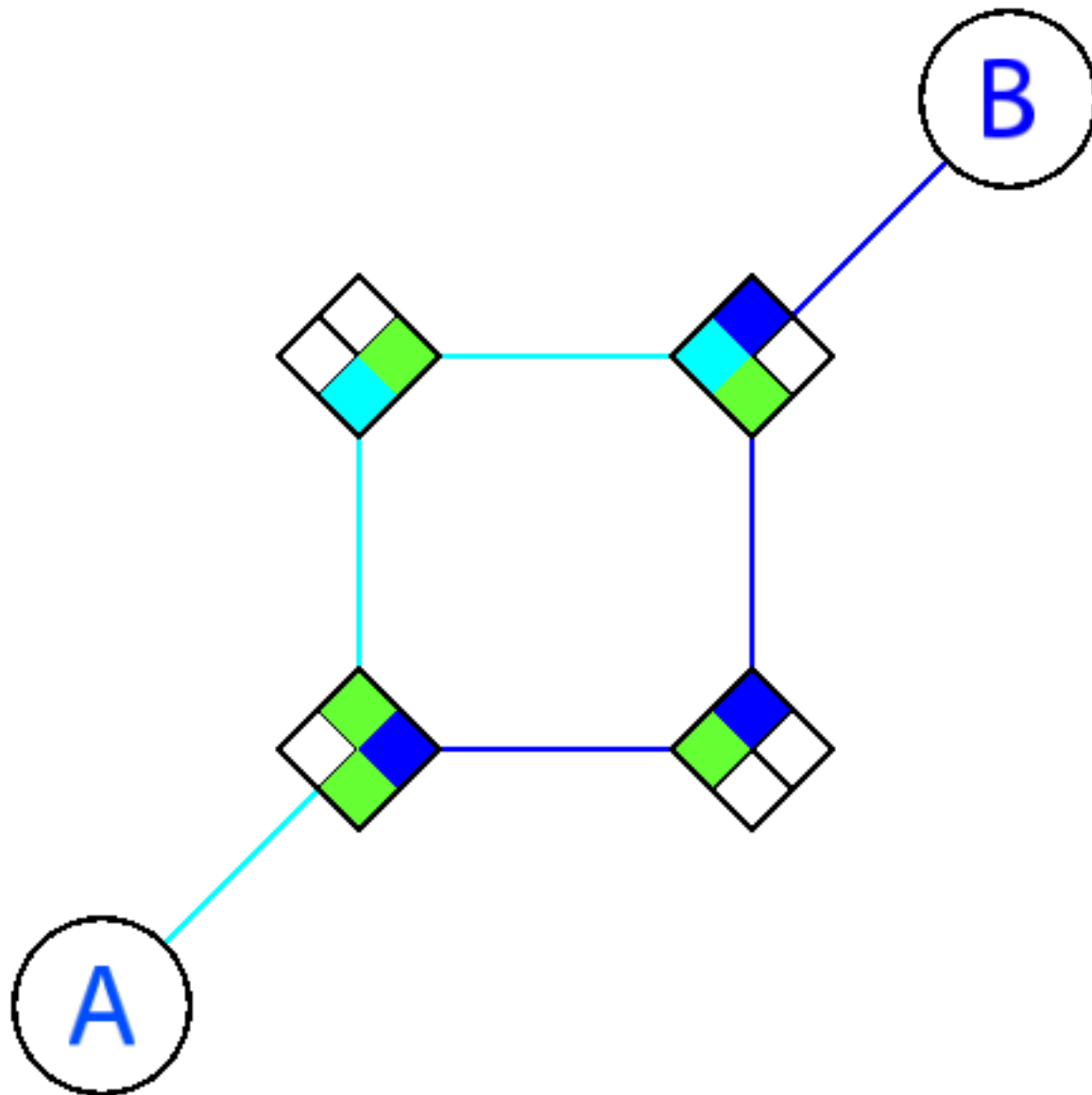




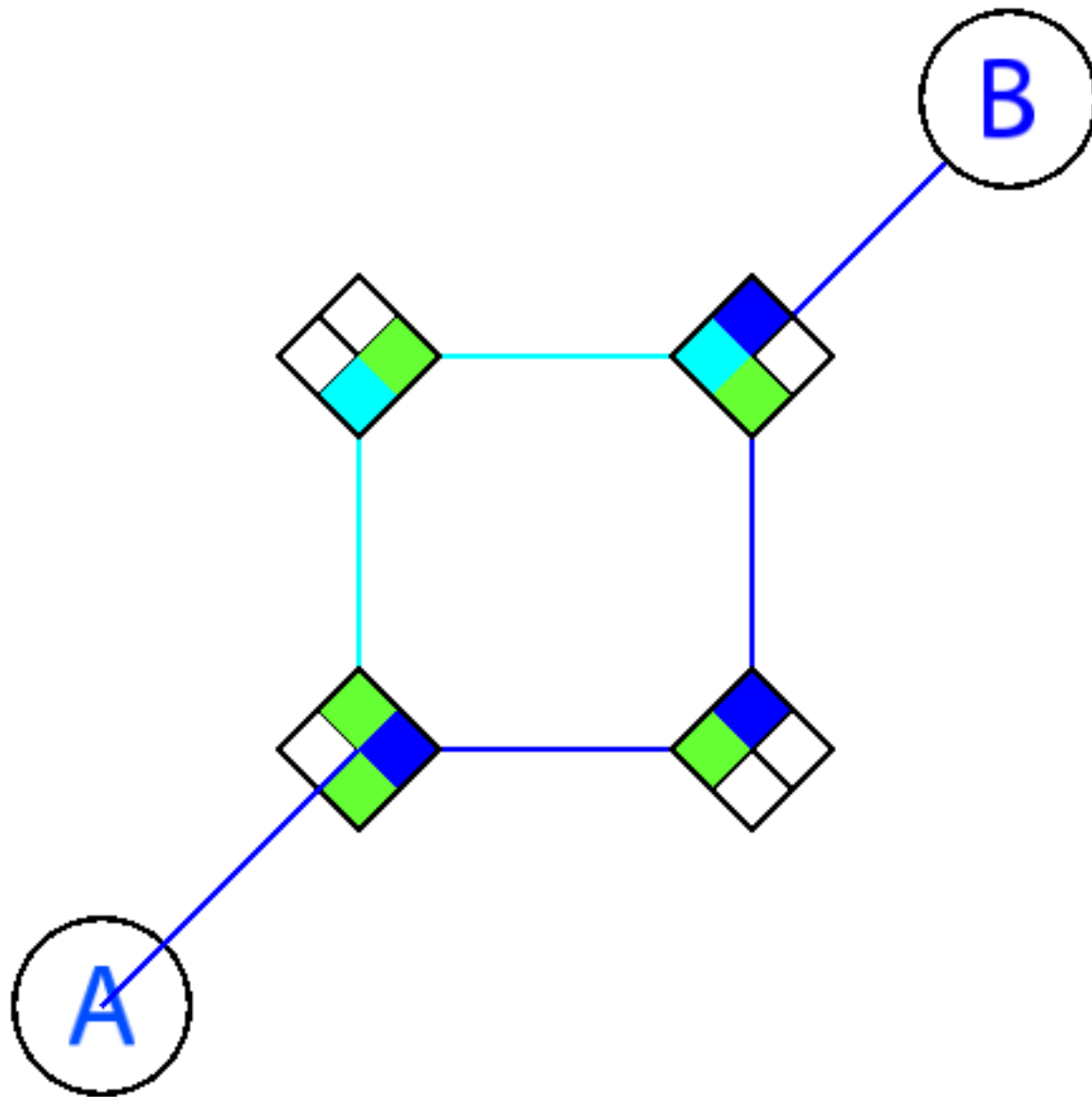
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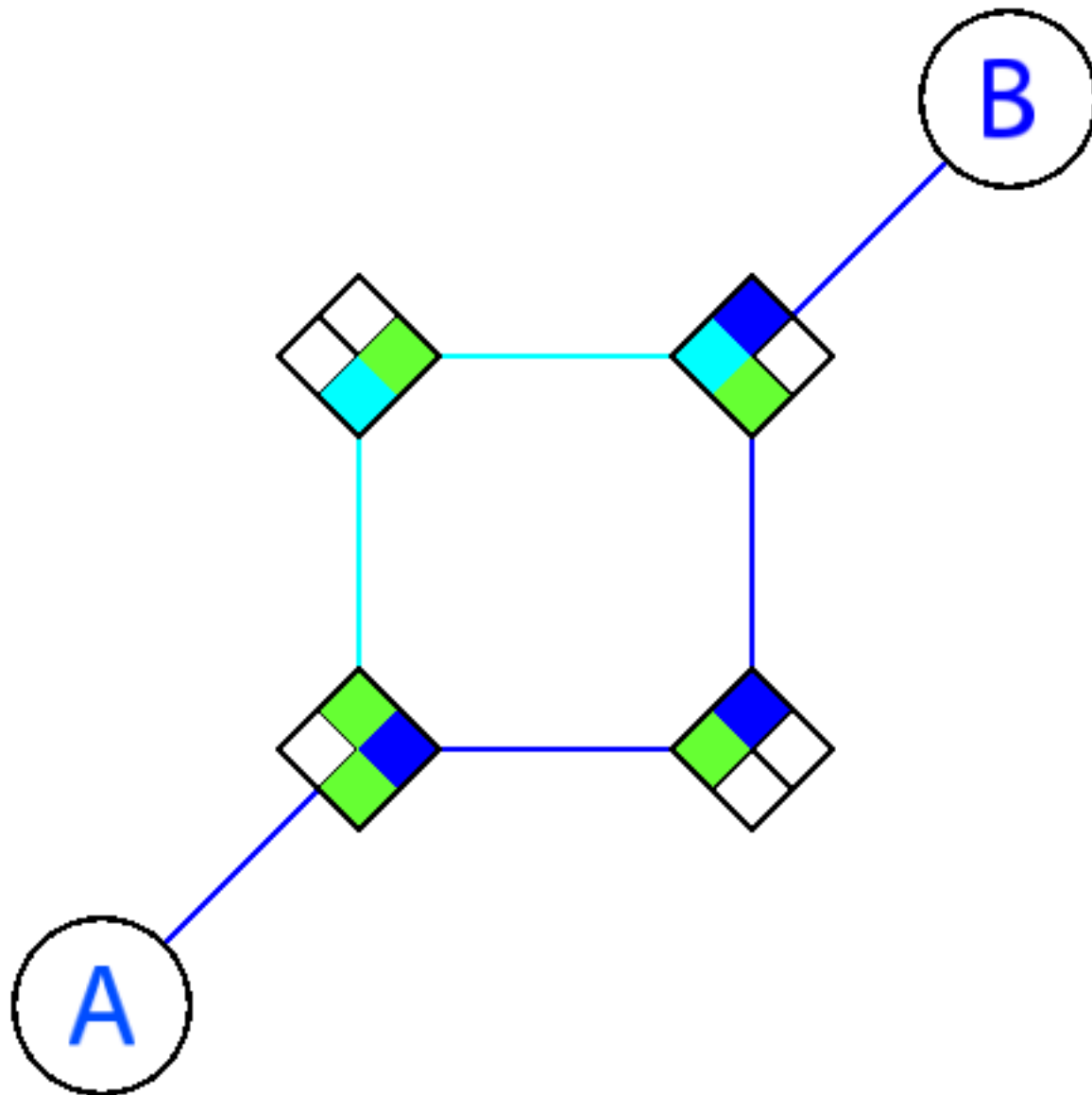
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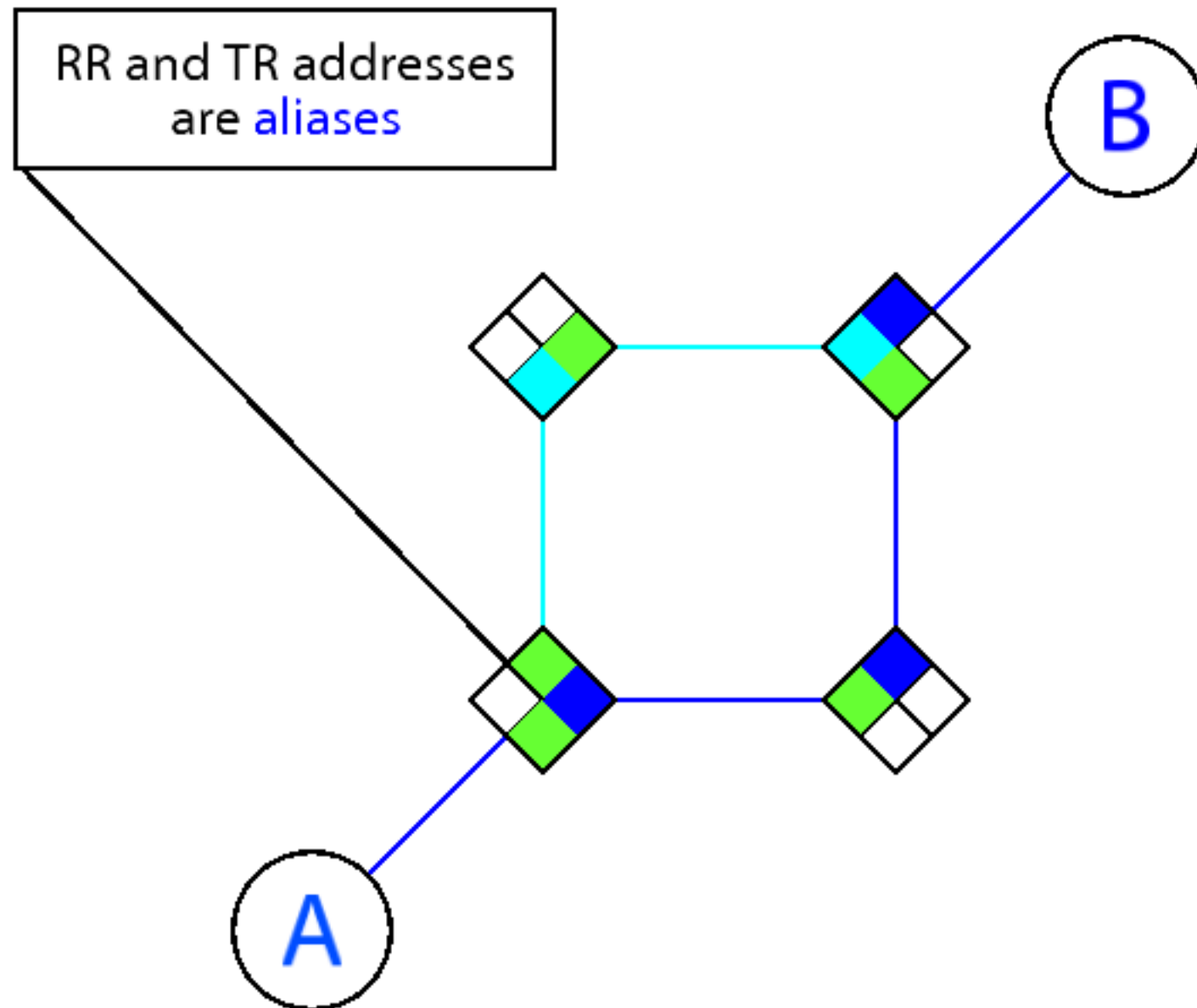
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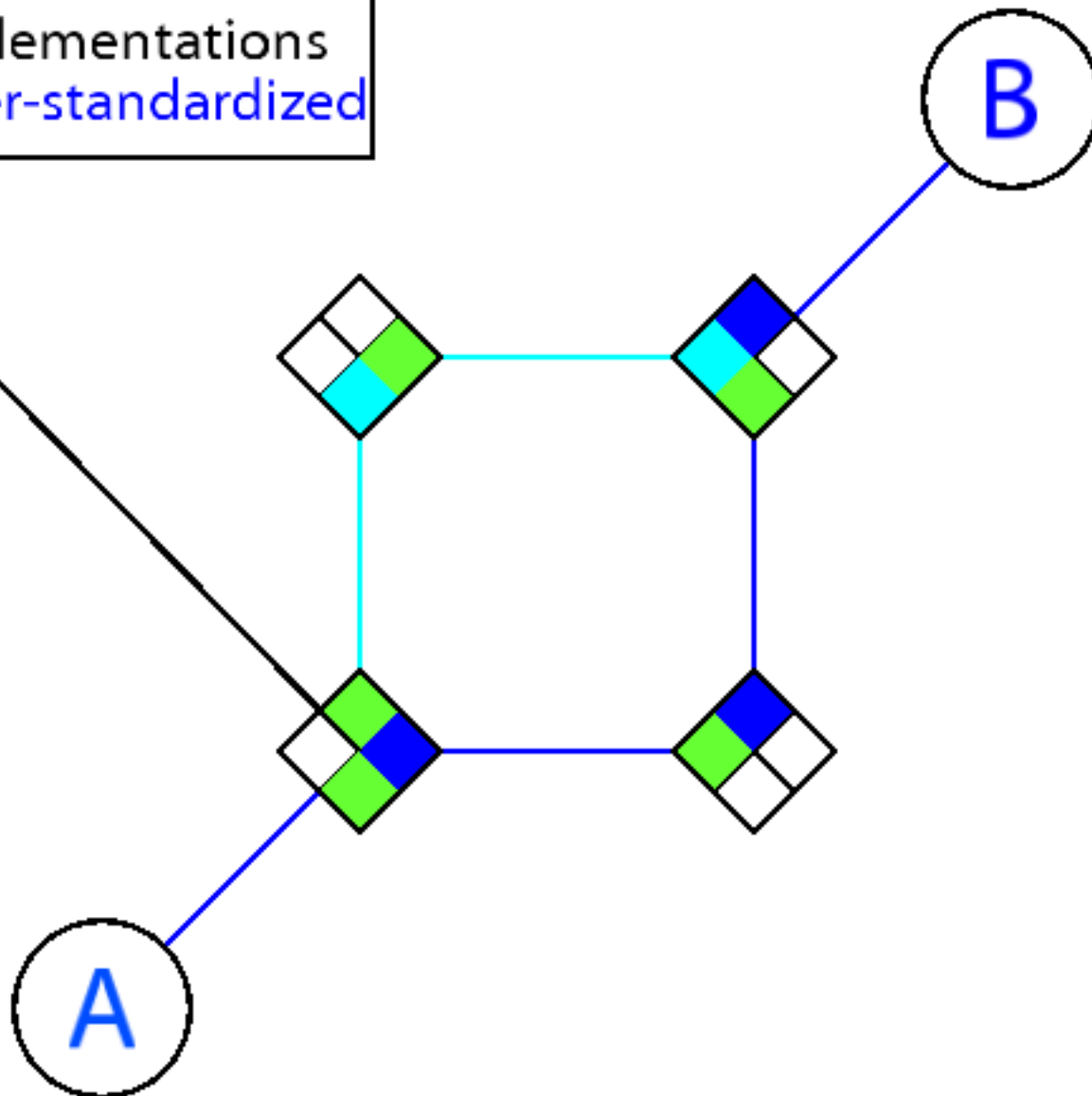


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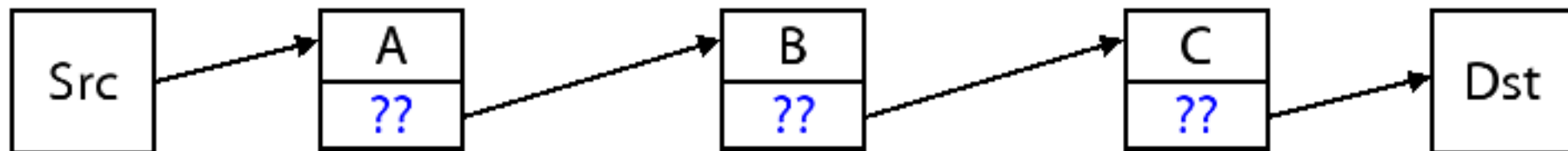
RR implementations are under-standardized



# RR Implementations

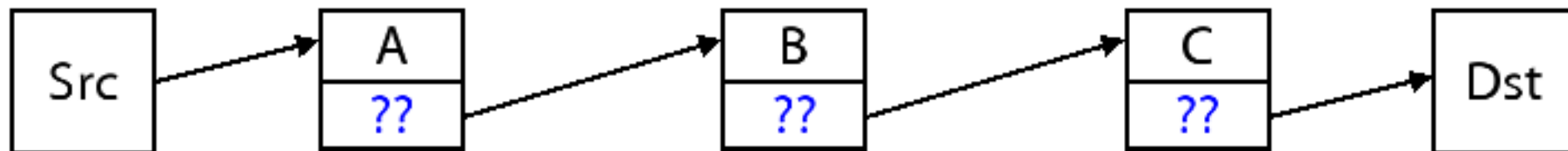
	TTL decremented before RR field updated	Interface	Decrement TTL
Departing (61.9)	Yes	Outgoing	Yes
MPLS (13.3%)	Like Departing, but does not implement RR on MPLS interfaces		
NotImpl (9.1%)	--	--	Yes
Arriving (7.1%)	No	Outgoing or loopback	Yes
Lazy (5.8%)	?	?	No if RR is set
Mixed (2.7%)	No	Outgoing, but incoming if TTL=1	Yes
Hidden (<1%)	?	--	No

# RR Diversity

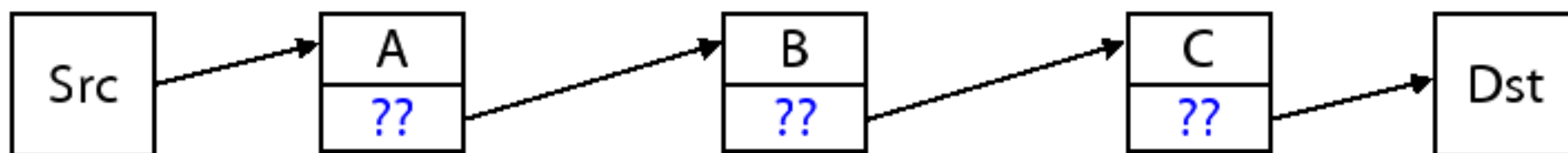




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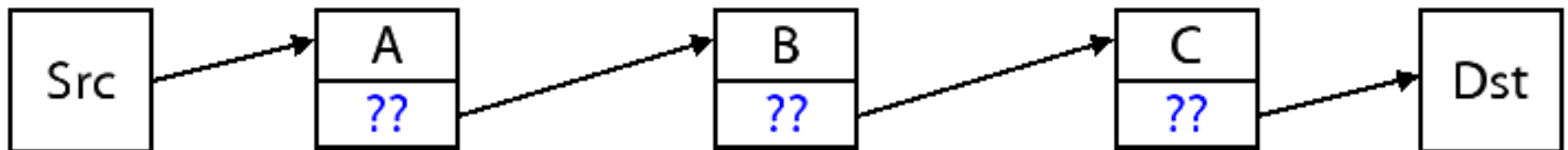


# RR Diversity



Record route

Traceroute

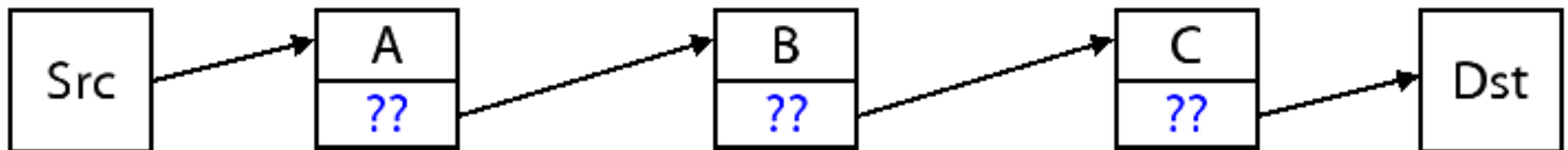


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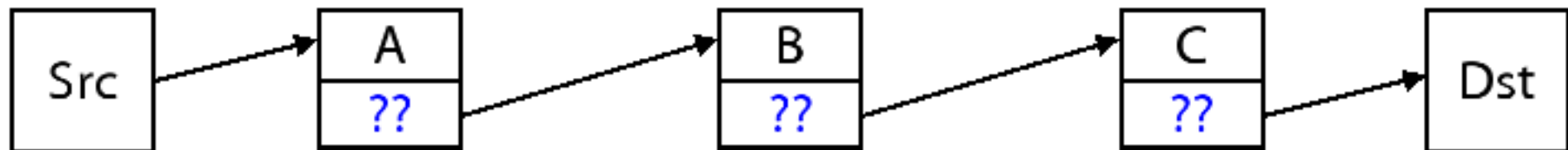


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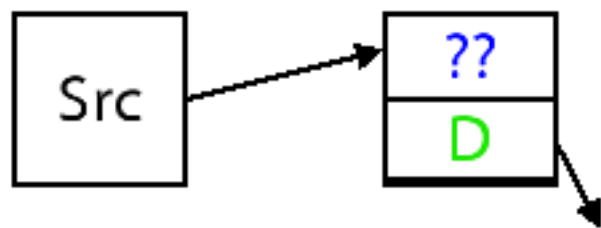


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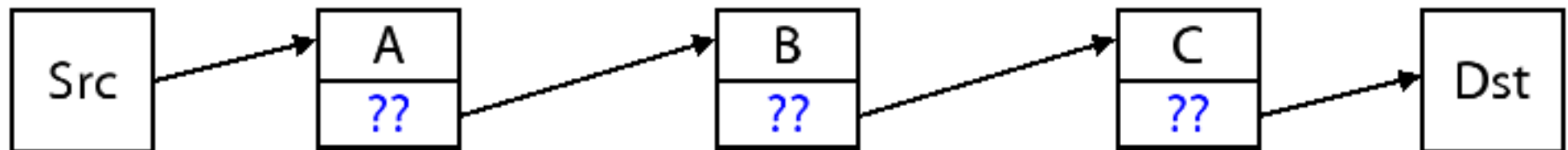


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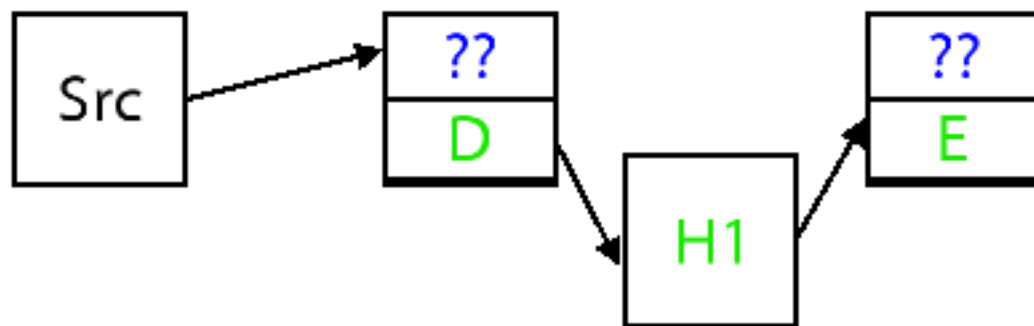


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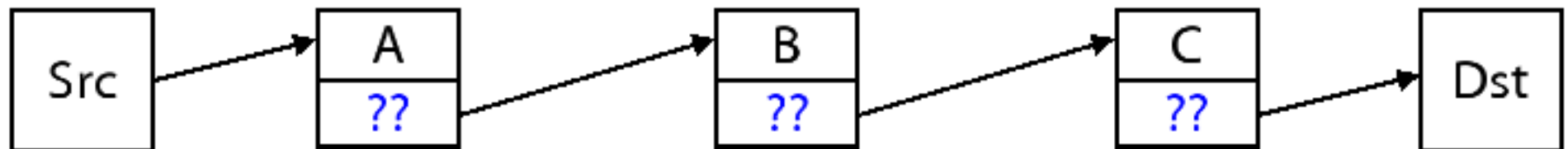


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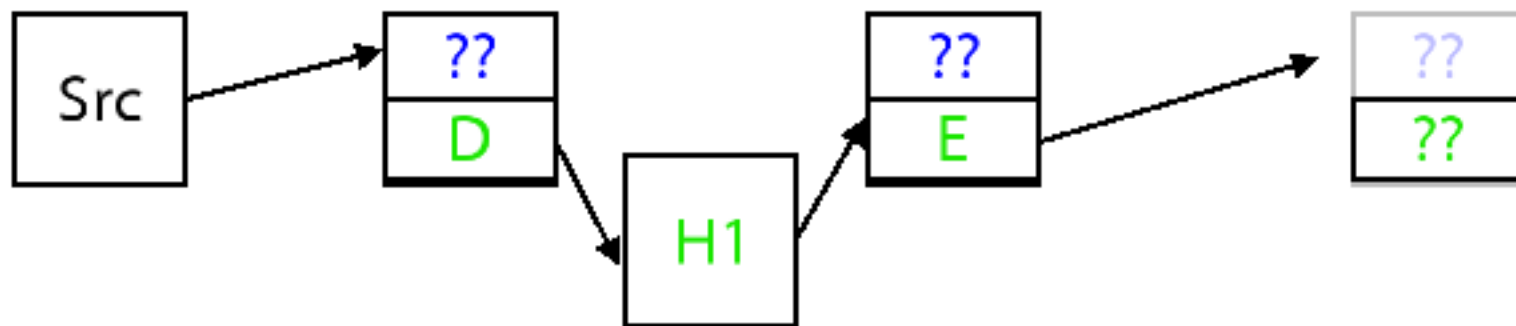


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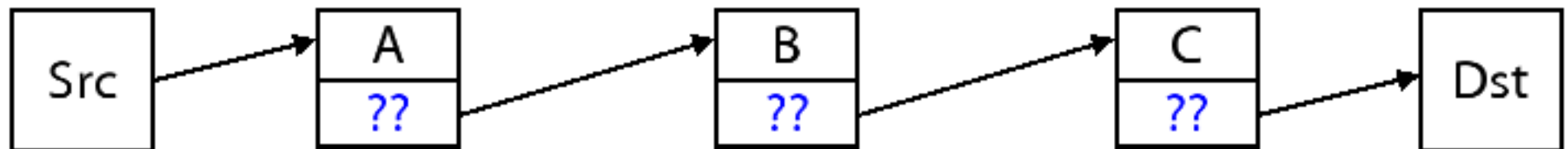


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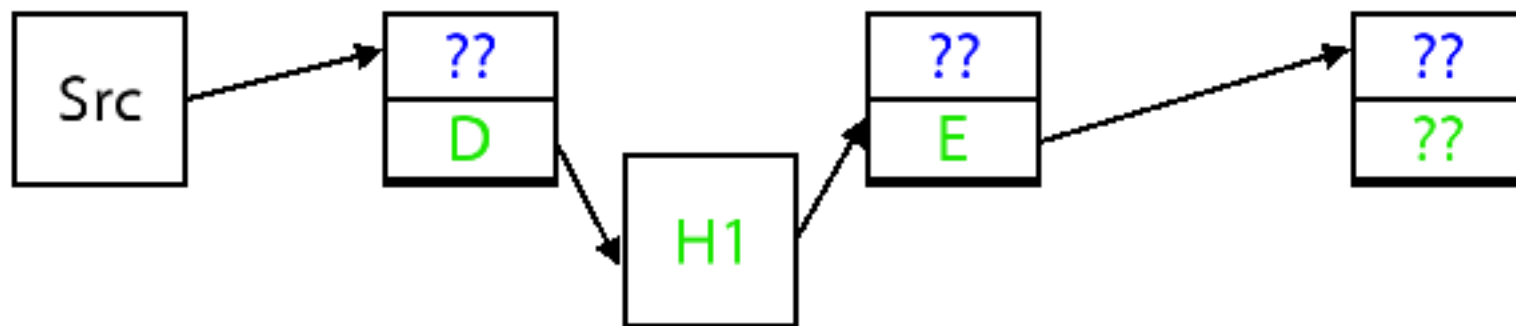
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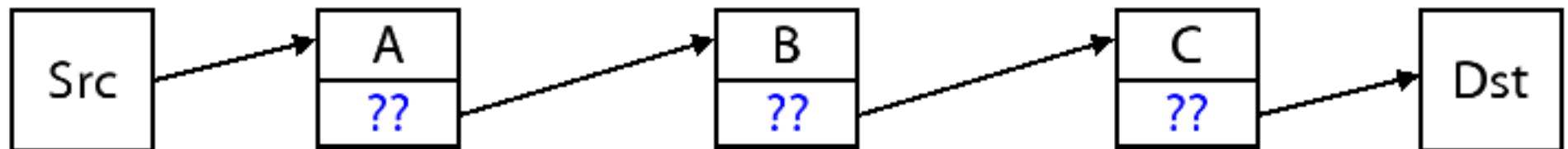


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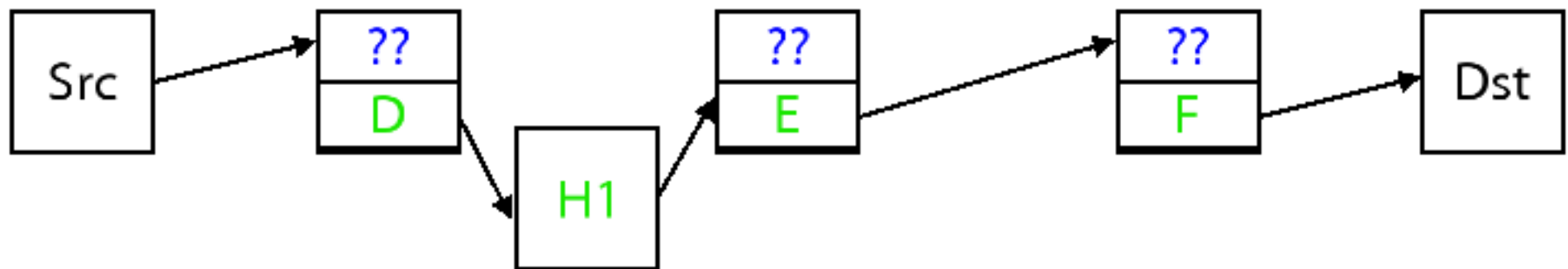


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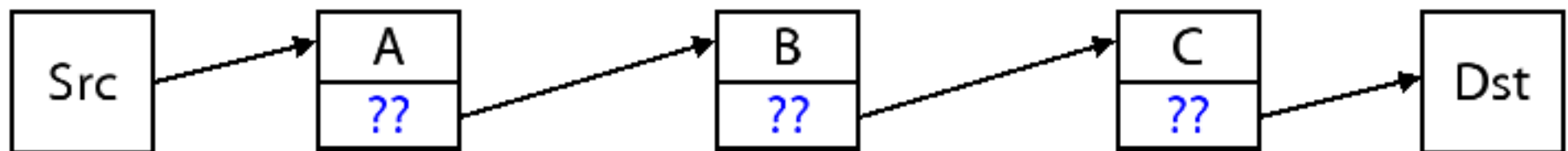


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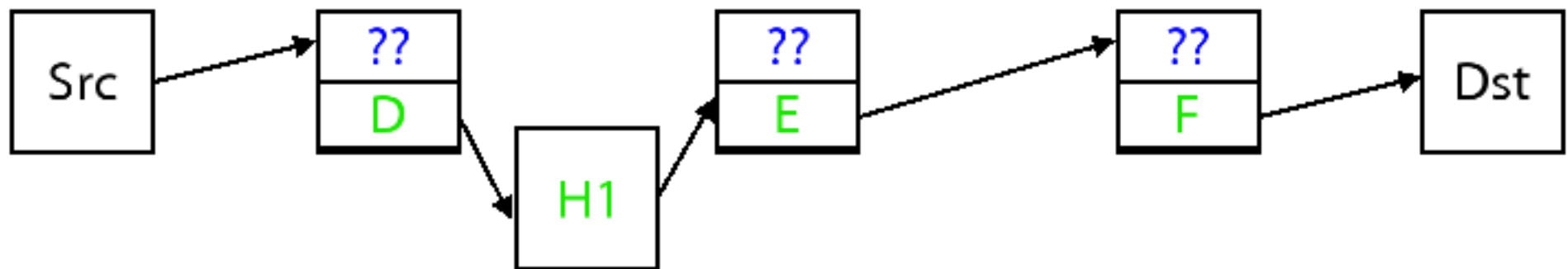


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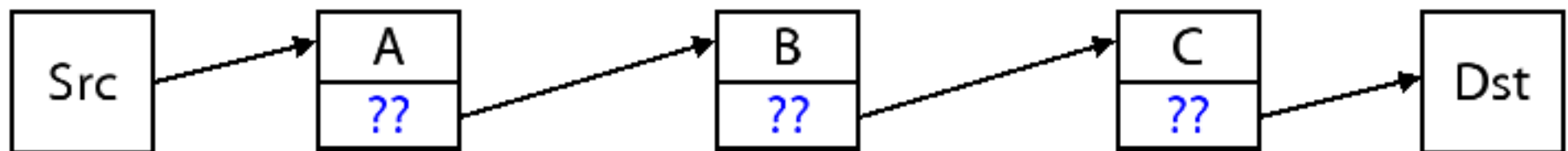


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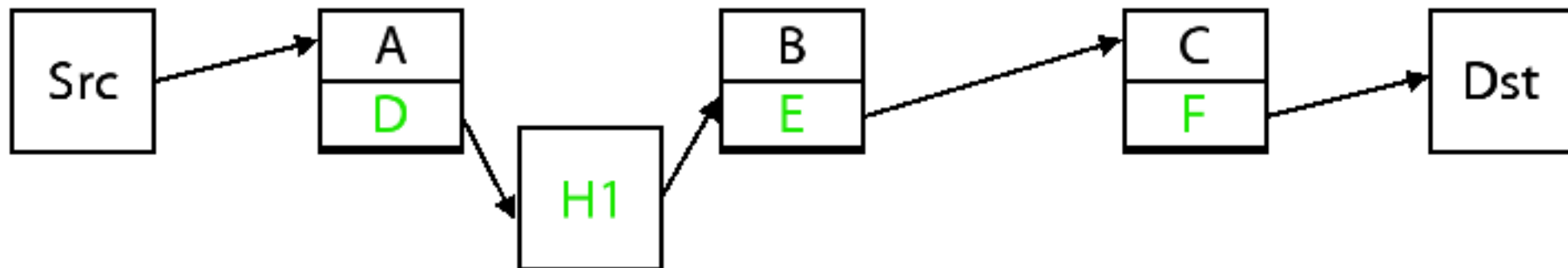


Record route

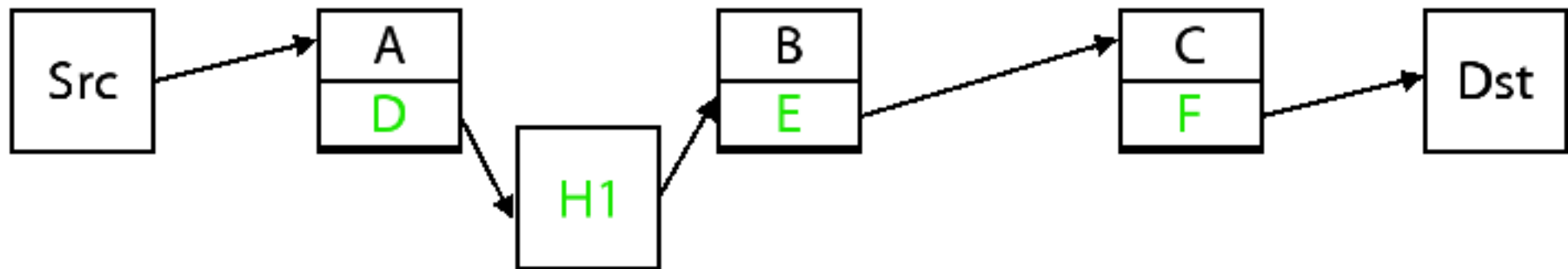
Traceroute



# Merged TR and RR



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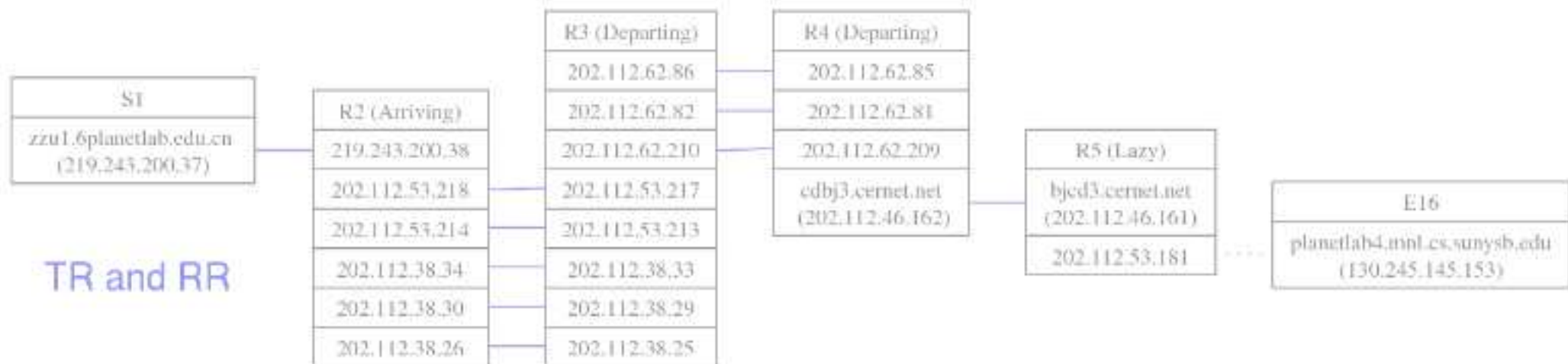


# RR is Hard to Use

- ◆ Implementation diversity adds ambiguity to RR and TR matching
  - Each traceroute hop adds 0-4 RR addresses
  - 7 implementations times 7 implementations
  - Alias resolution requires implementation classification
  - Bad classifications lead to bad aliases
- ◆ RR probes take different paths

# Example Merge

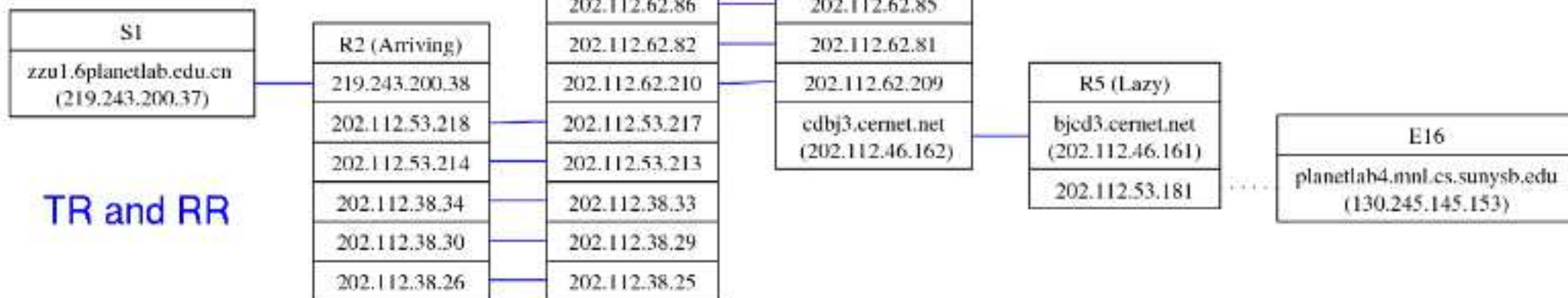
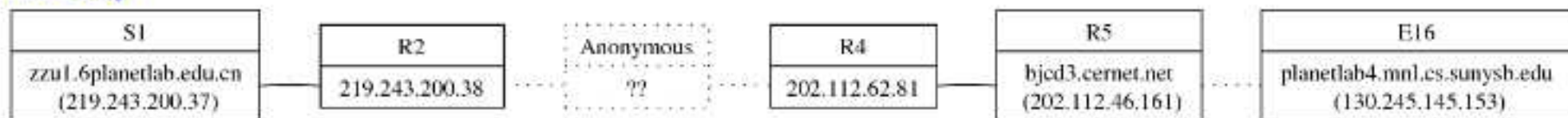
TR only





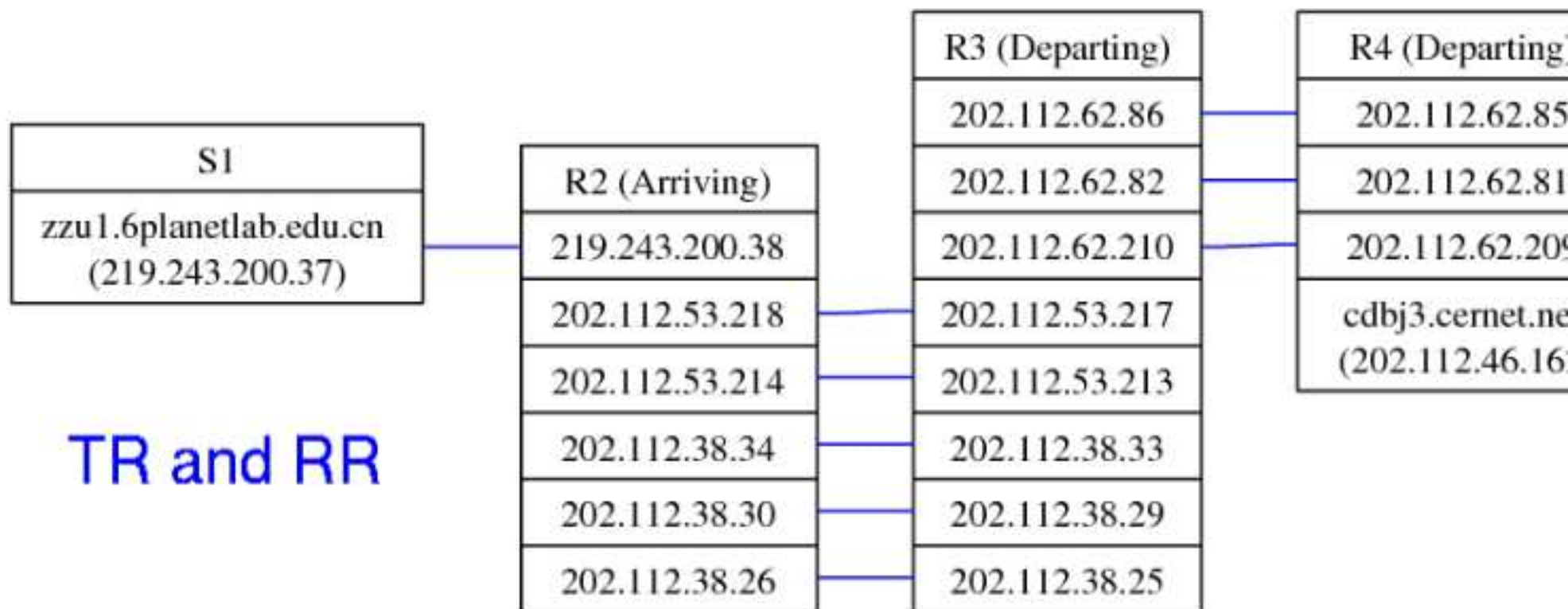
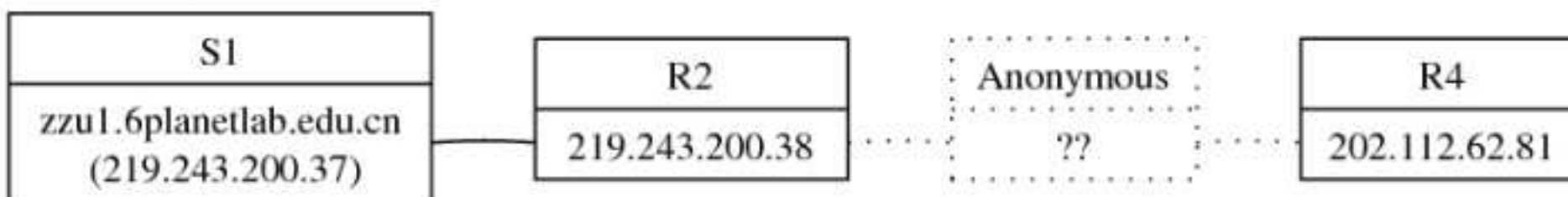
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TR only



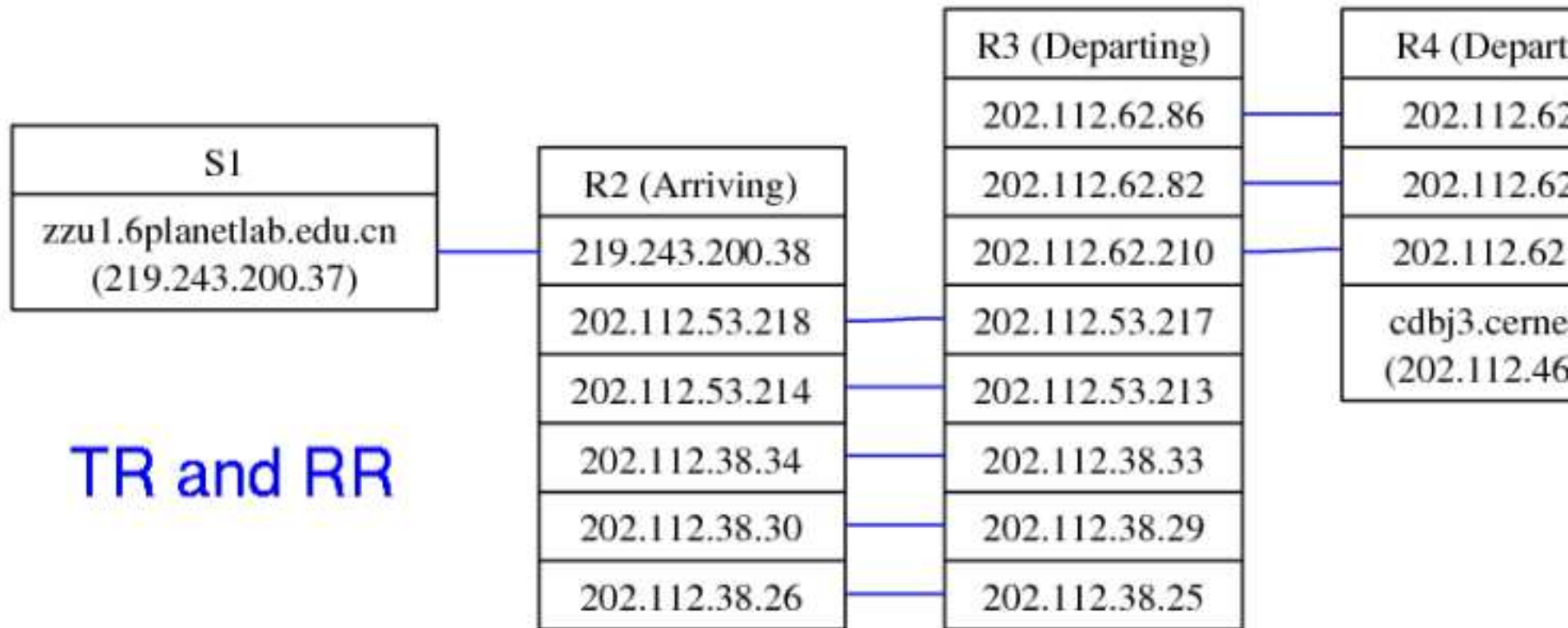
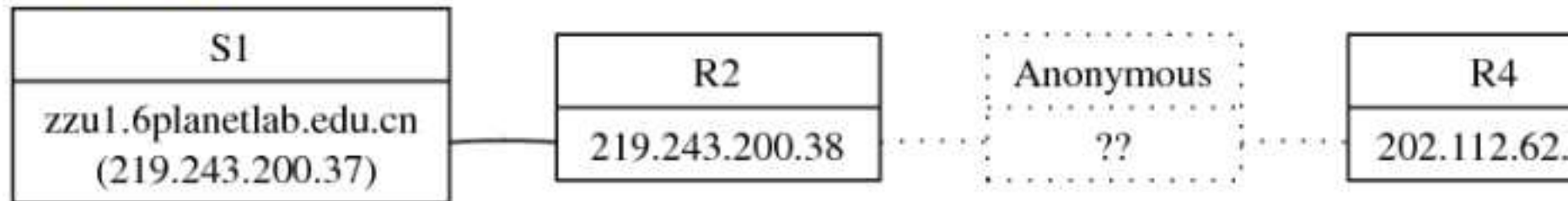
TR and RR

## TR only

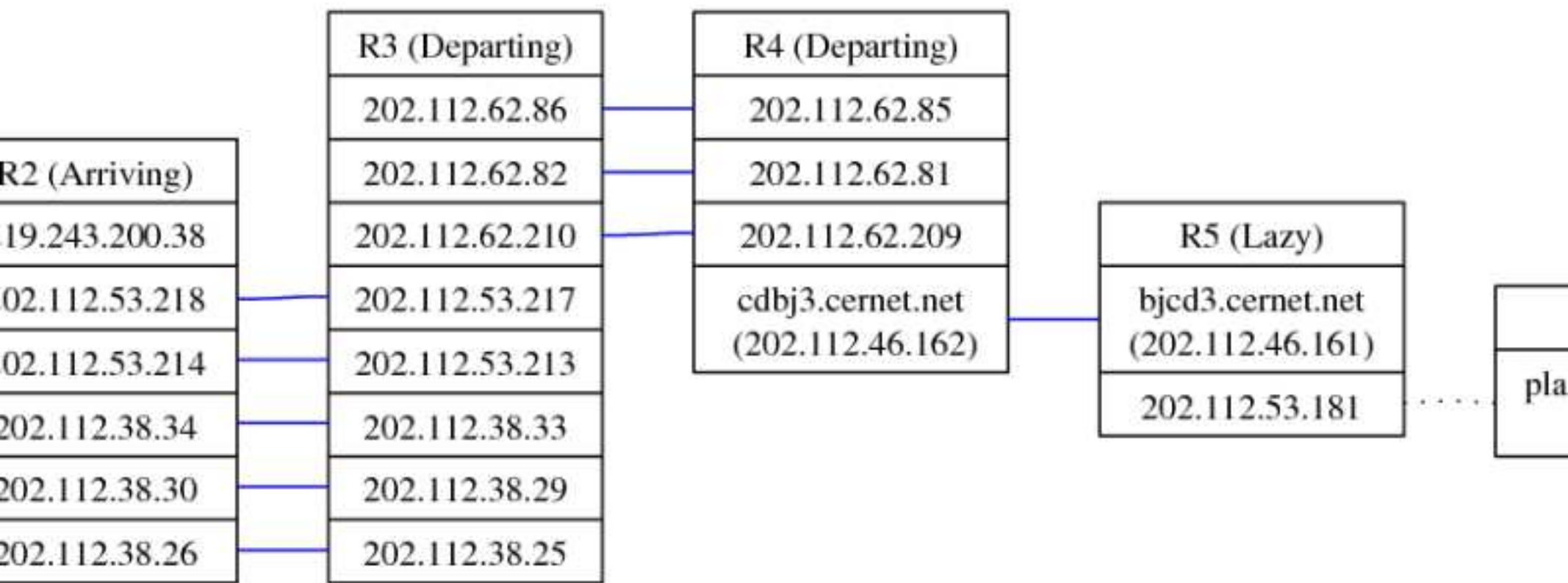
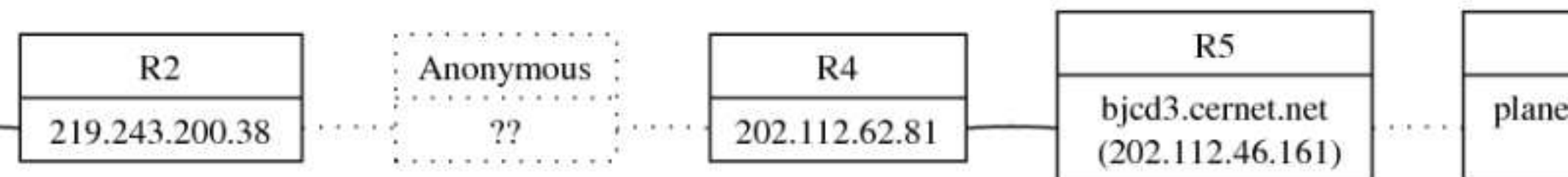


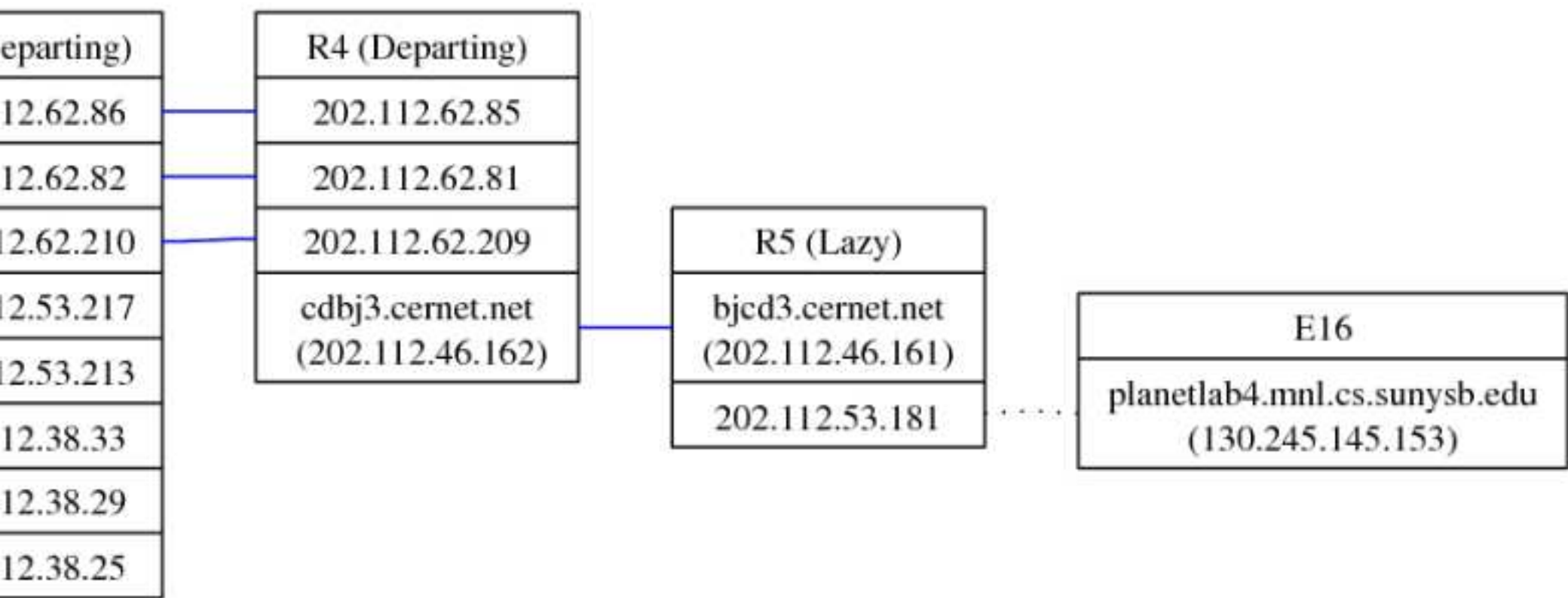
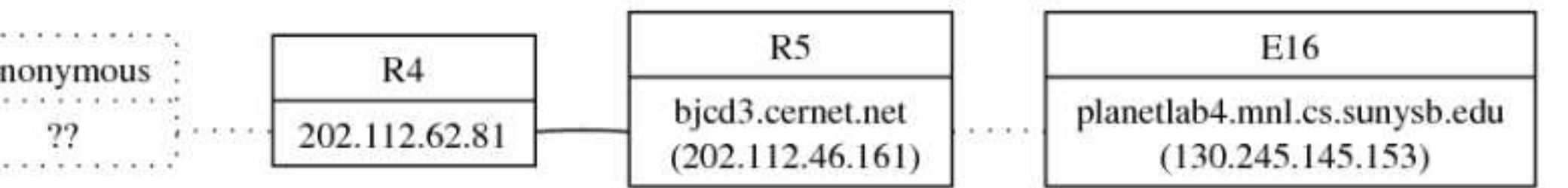
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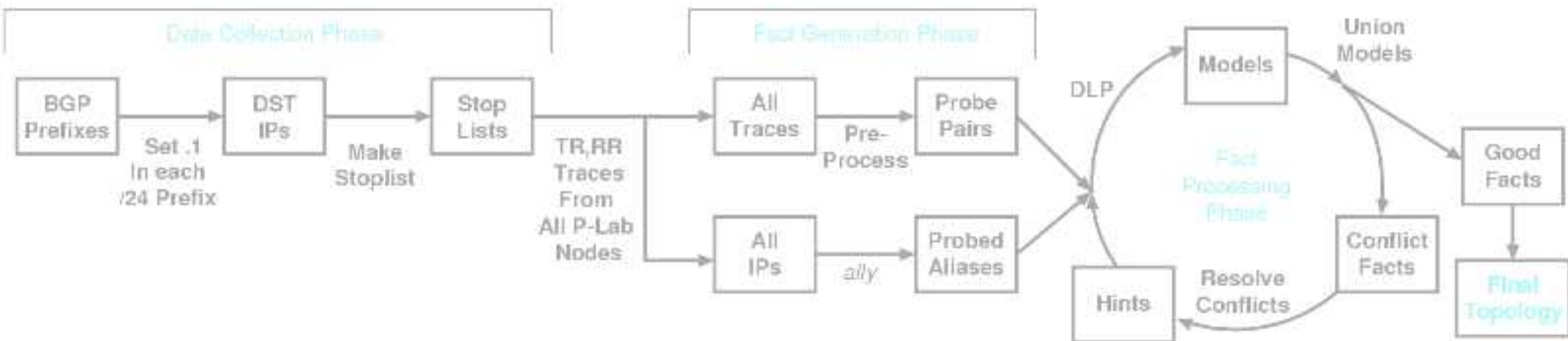


## TR and RR

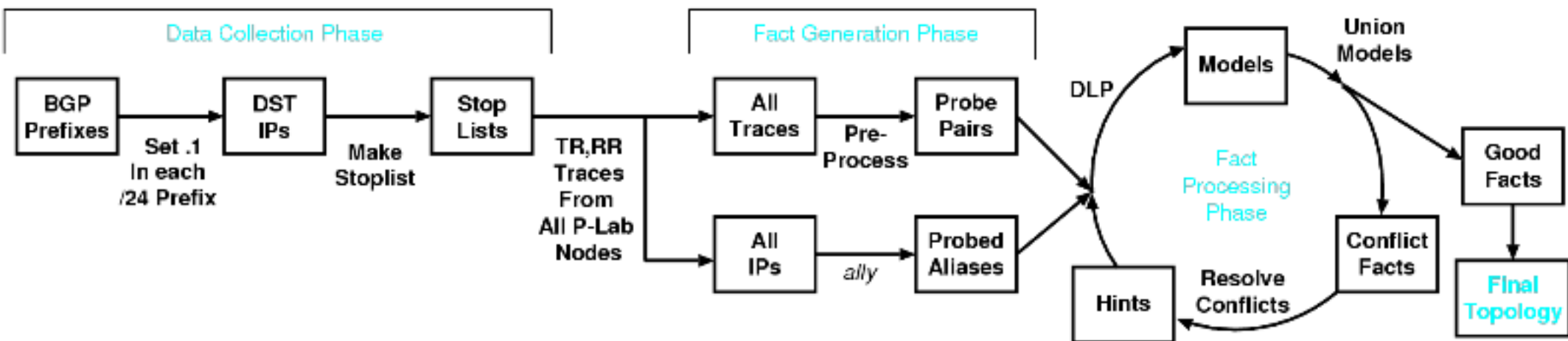


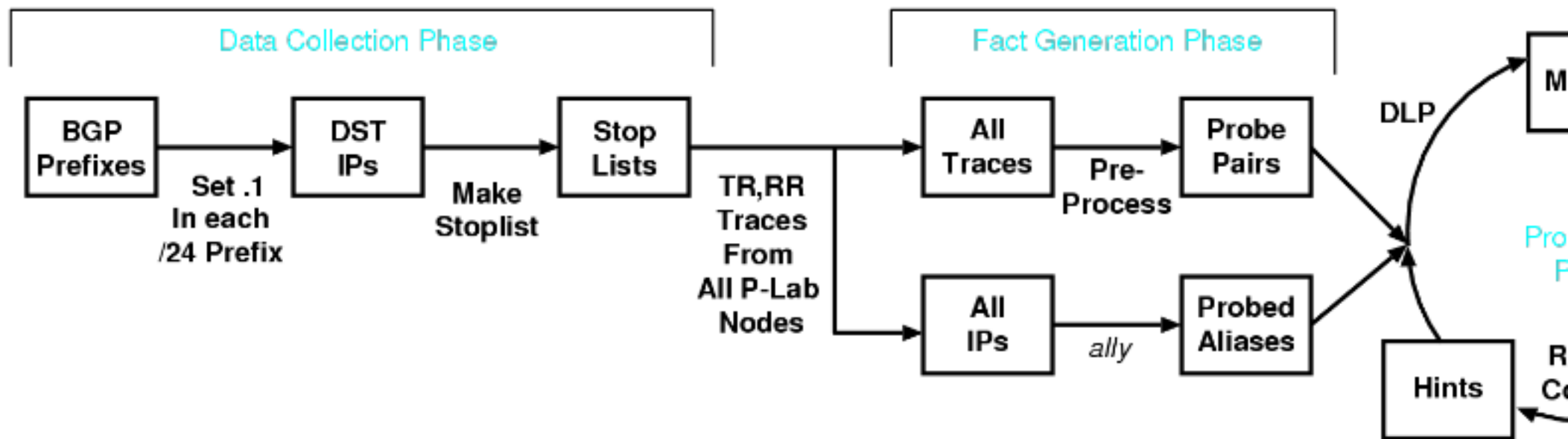


# DisCarte System

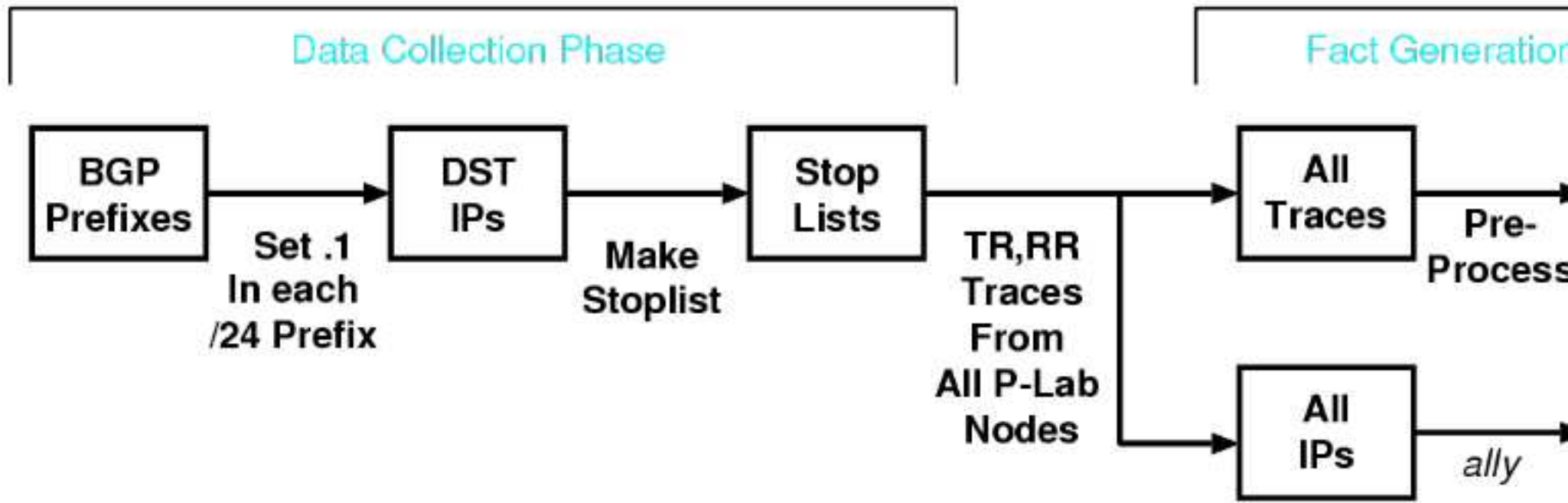


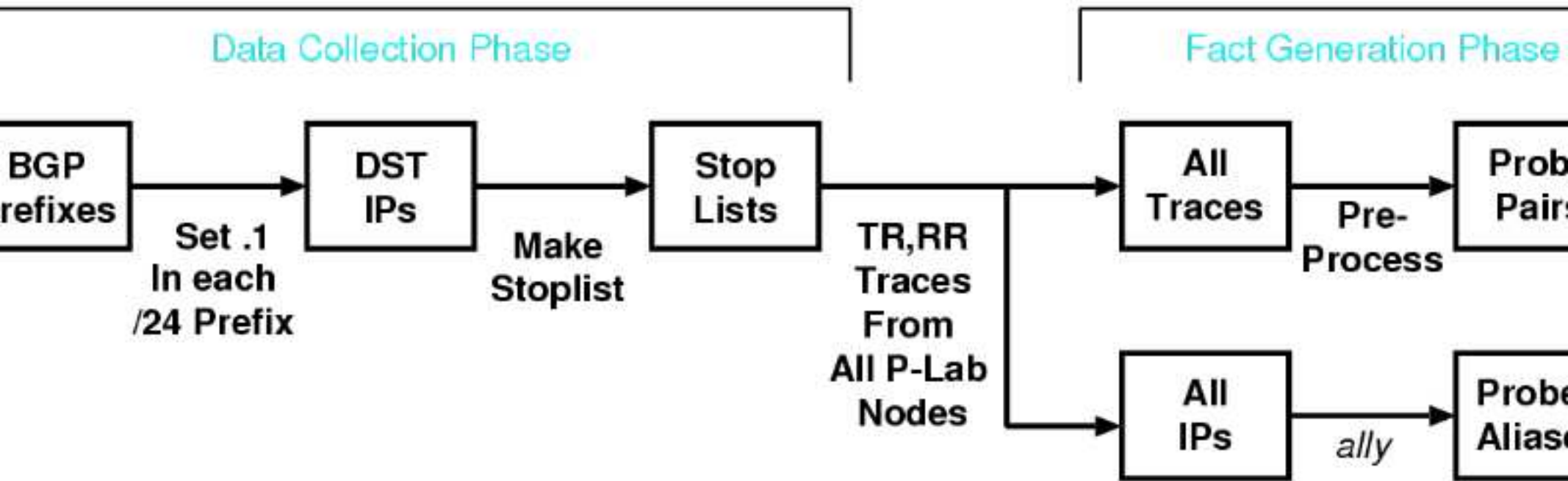
# DisCarte System

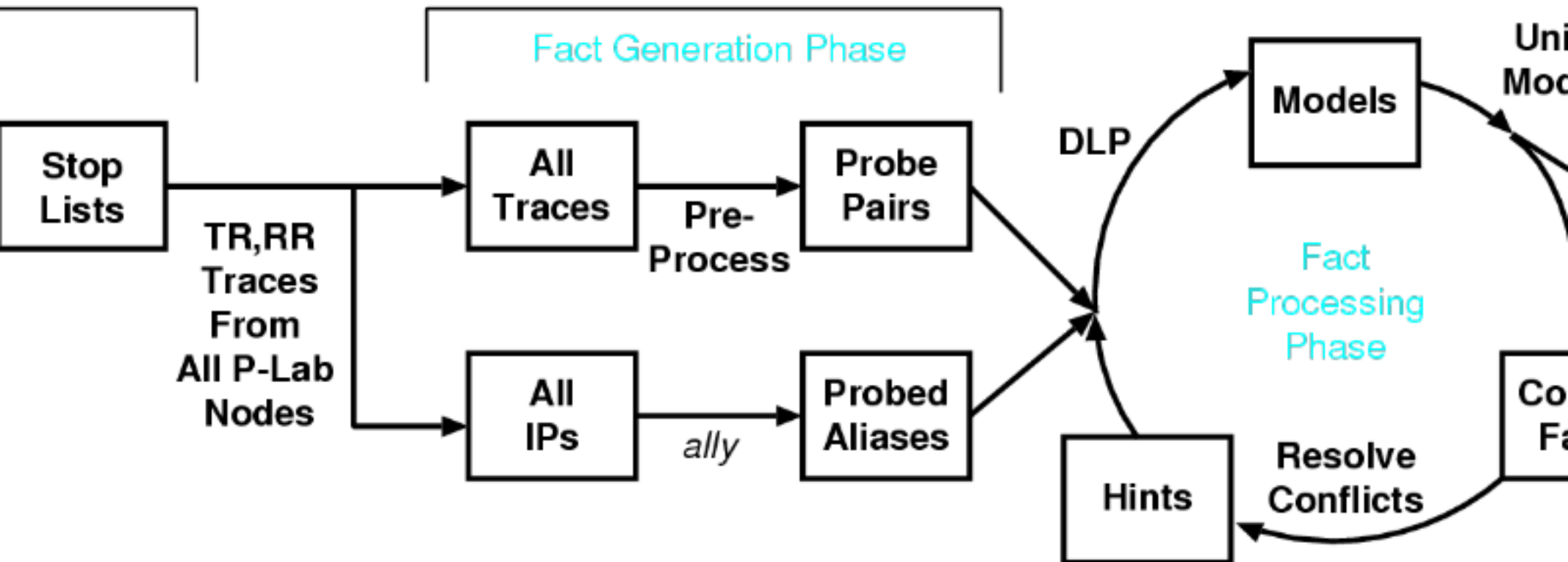


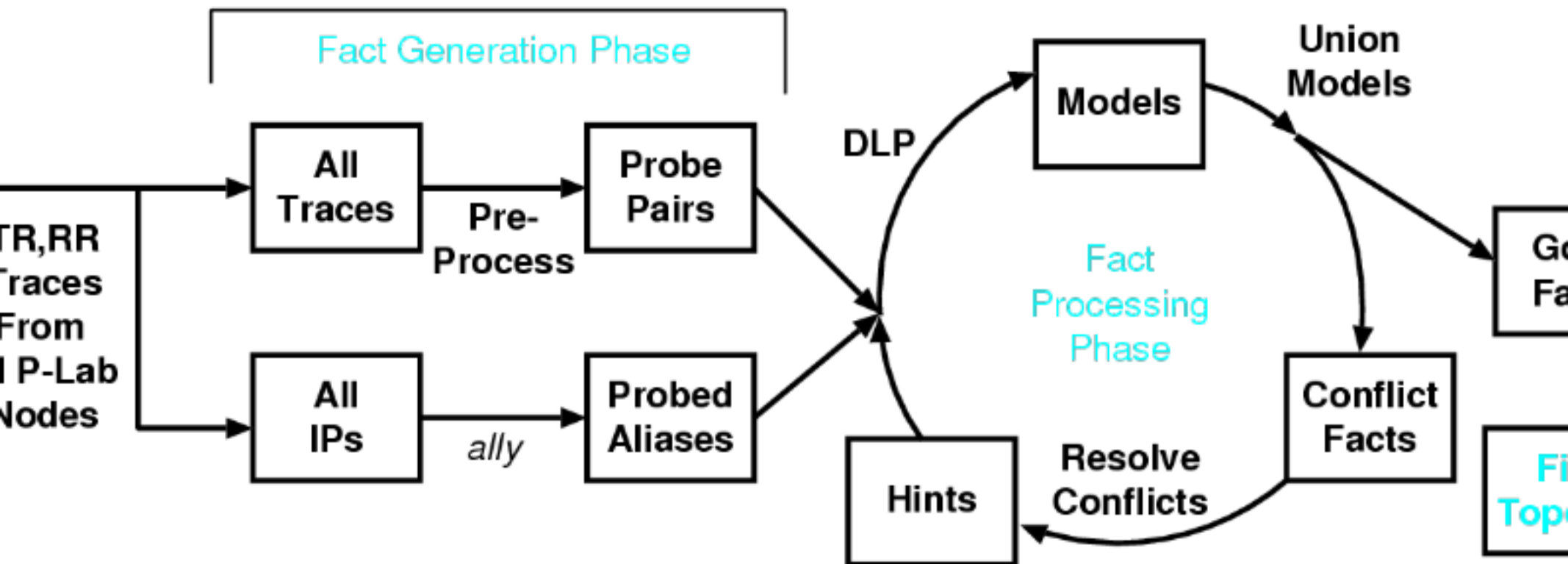


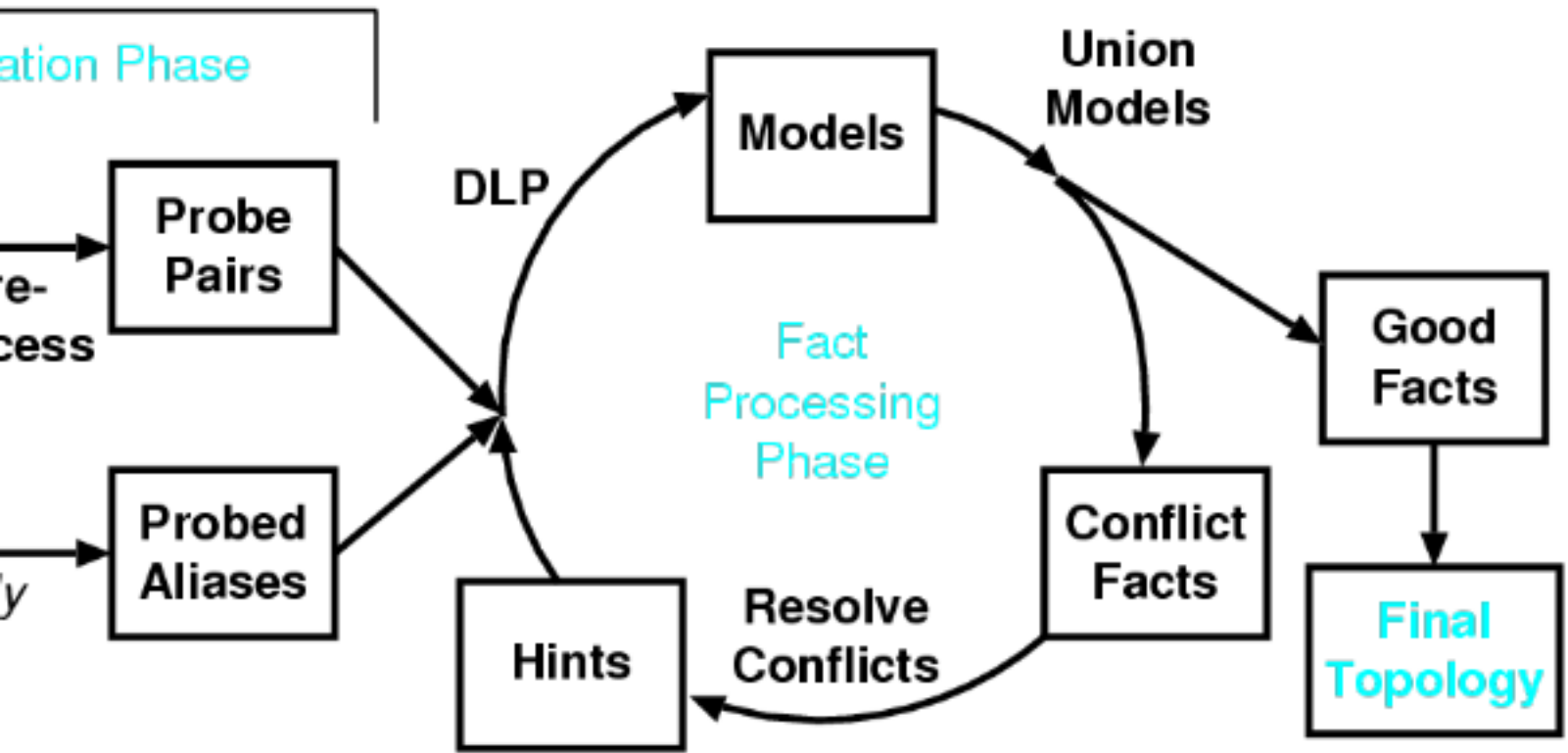














# Disjunctive Logic Programs

- ◆ DLP: Inference engine and constraint solver
- ◆ **Facts:** TR and RR measurement
  - `probe(src=128.8.128.118, dst=128.208.4.198, ttl=9, resp=206.196.178.90)`
- ◆ **Inference rules**
  - 0 new RR entries → Arriving to Departing, NotImpl to NotImpl, etc...
- ◆ **Constraints:**
  - Observed engineering practices





# Engineering Practices

Prefer models that violate fewest beliefs

- ◆ Routers rarely have self-loops
- ◆ Linked IPs are typically off-by-one
- ◆ Prior techniques are most often correct
- ◆ Hidden routers are uncommon

# Scaling Challenges

- ◆ Data collections
  - 387 sources, 376,408 destinations
  - 100,256 routers found
- ◆ Fact generation
  - 1.3 billion trace facts
- ◆ 1475 lines of DLP code
- ◆ DLP has exponential runtime

# Divide and Conquer

- ◆ Collect multiple views of the same topology features
  - Single view results in too many conflicts
- ◆ Keep inputs facts sizes manageable
- ◆ Aggregate output facts
- ◆ Work out potential disagreements

# Divide and Conquer

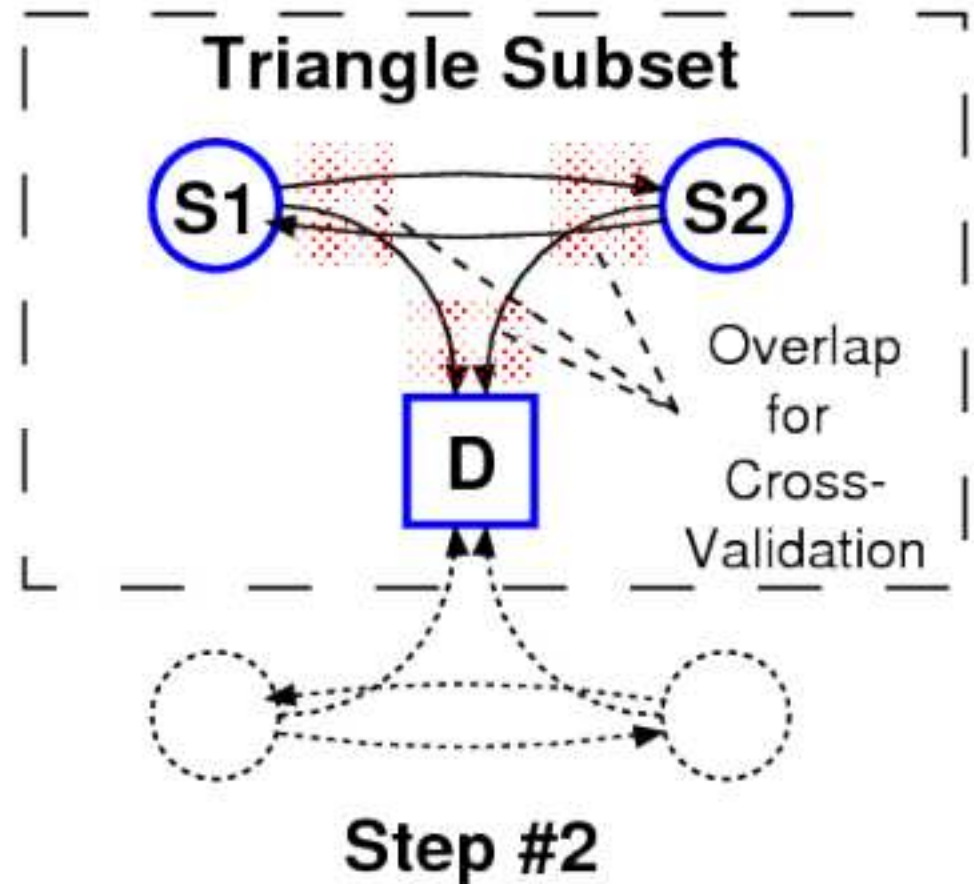
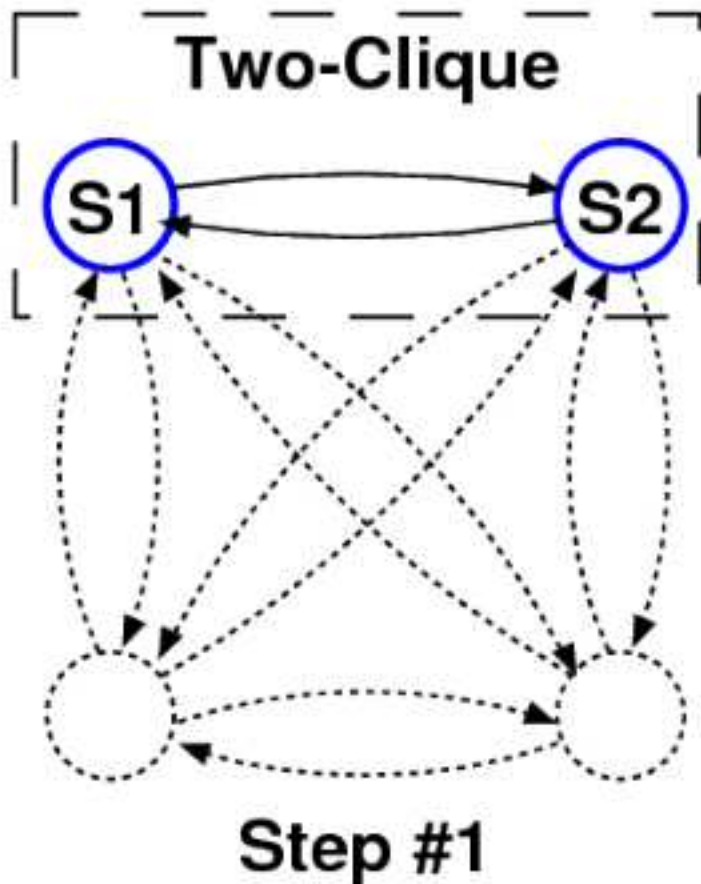
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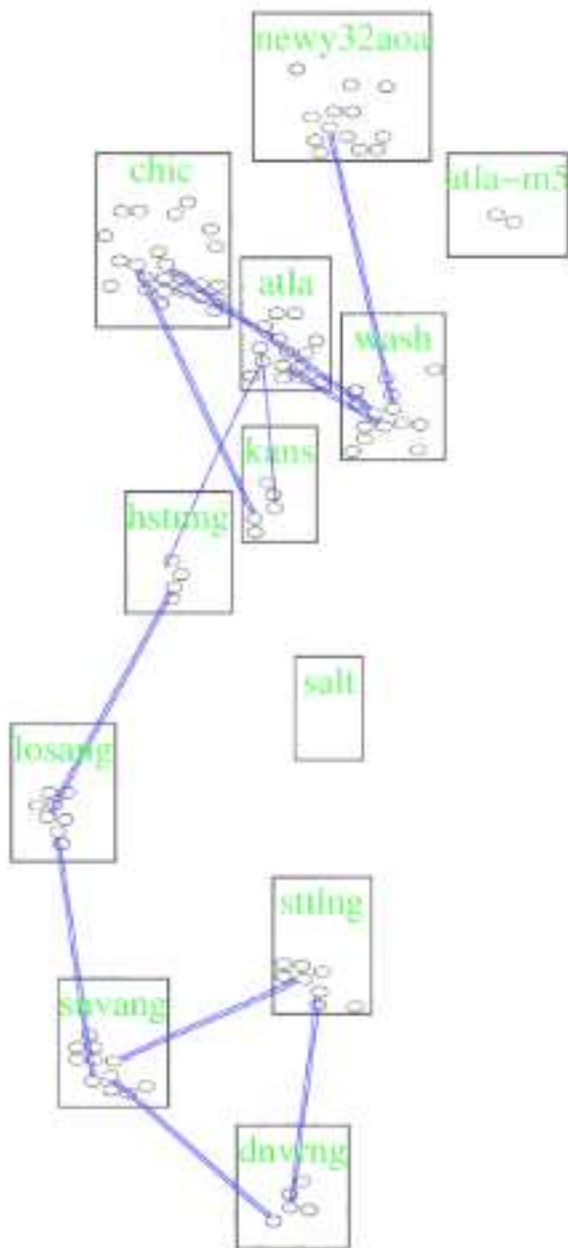
# Divide and Conquer Scheme

- ◆ Conflicts reduced from 1,547 to 28

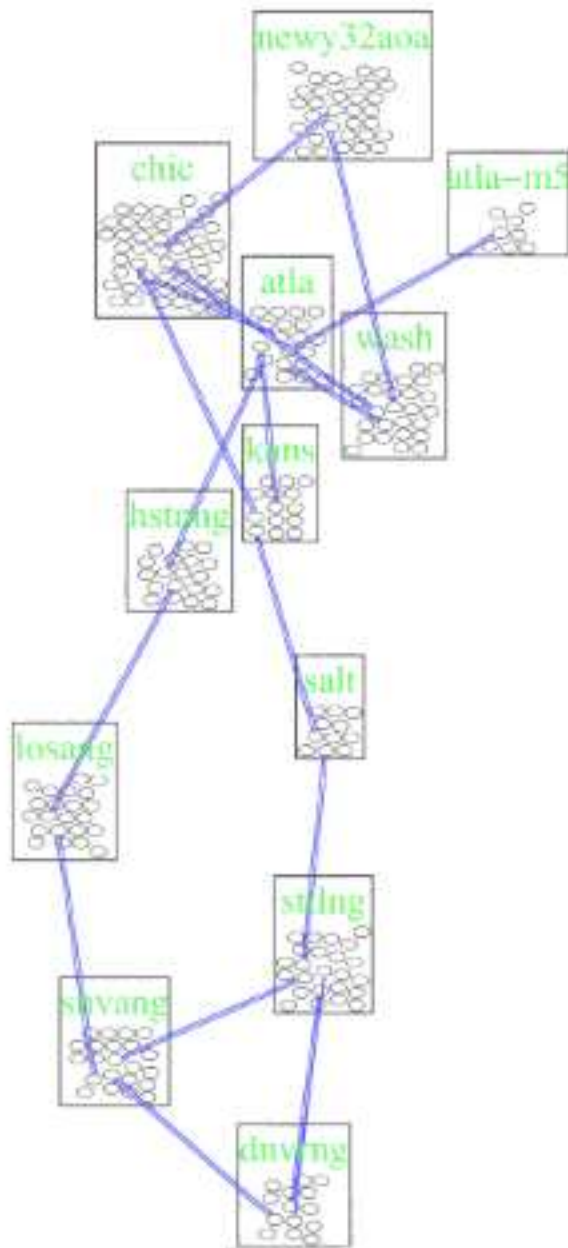


# Abilene Network

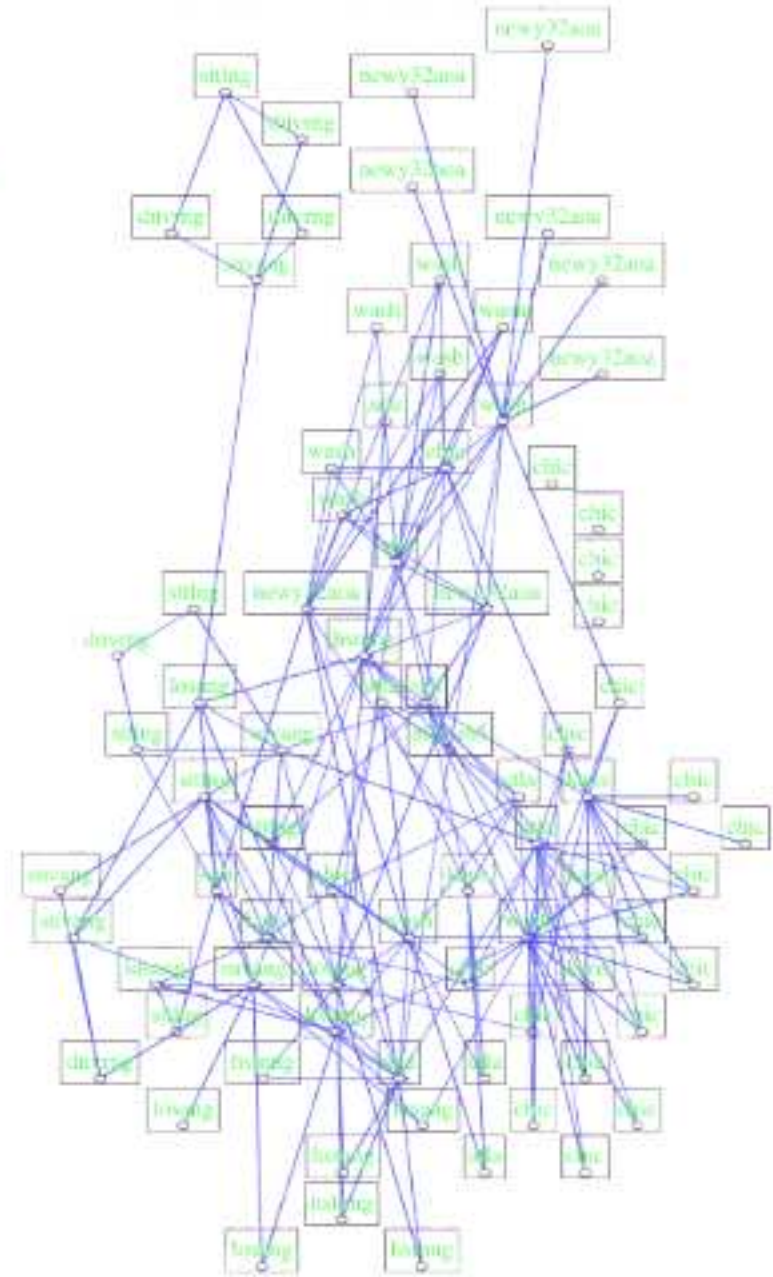
DisCarte



Actual

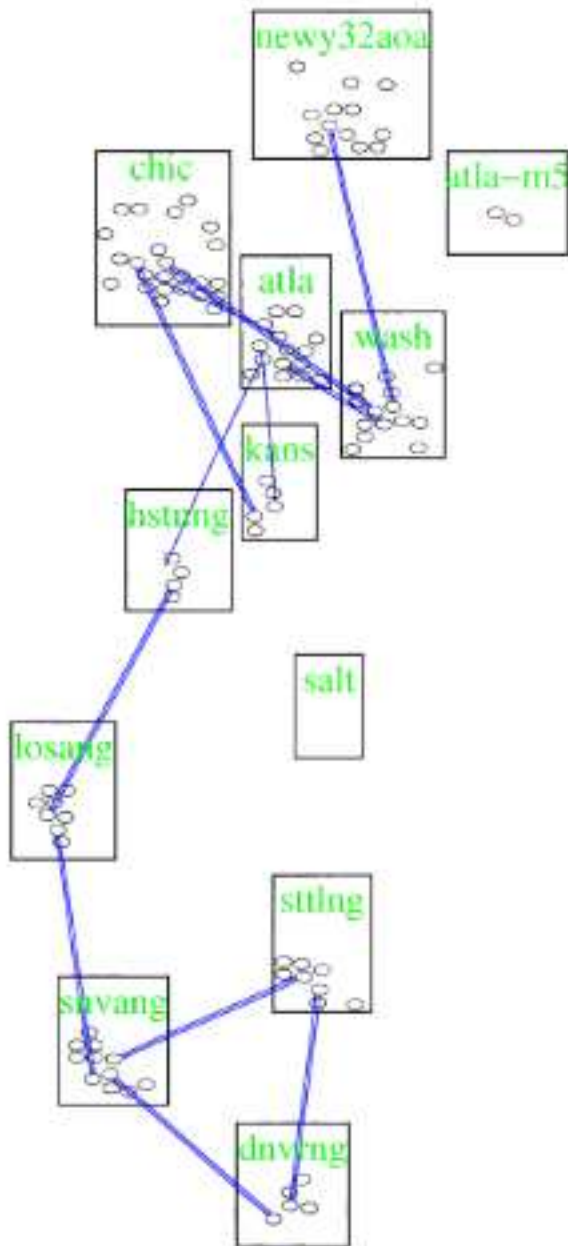


Rocketfuel

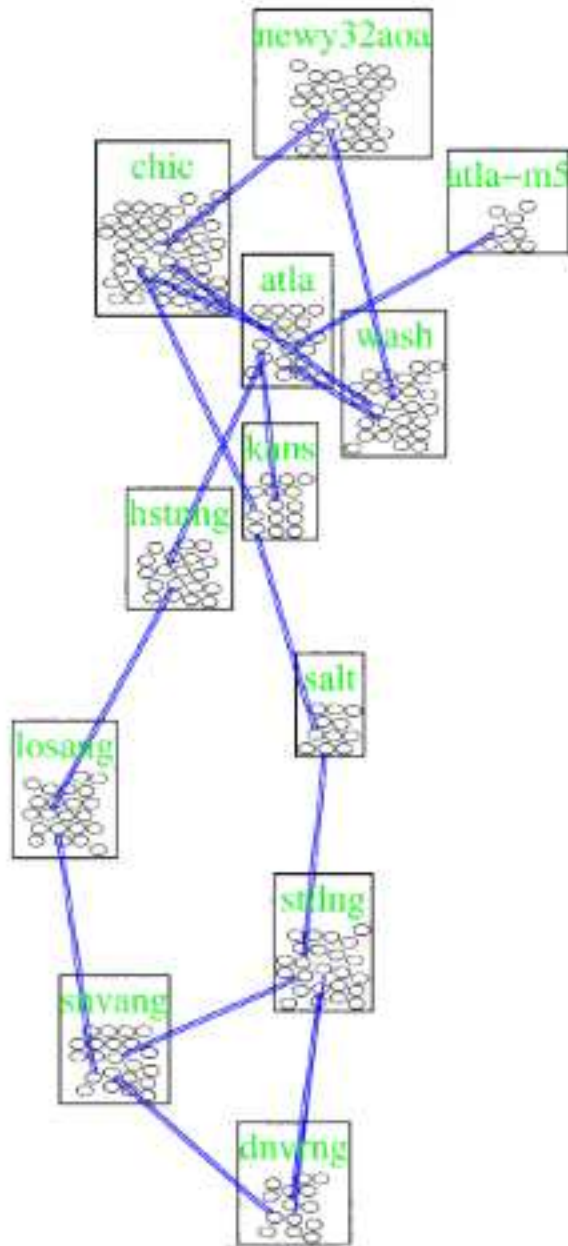


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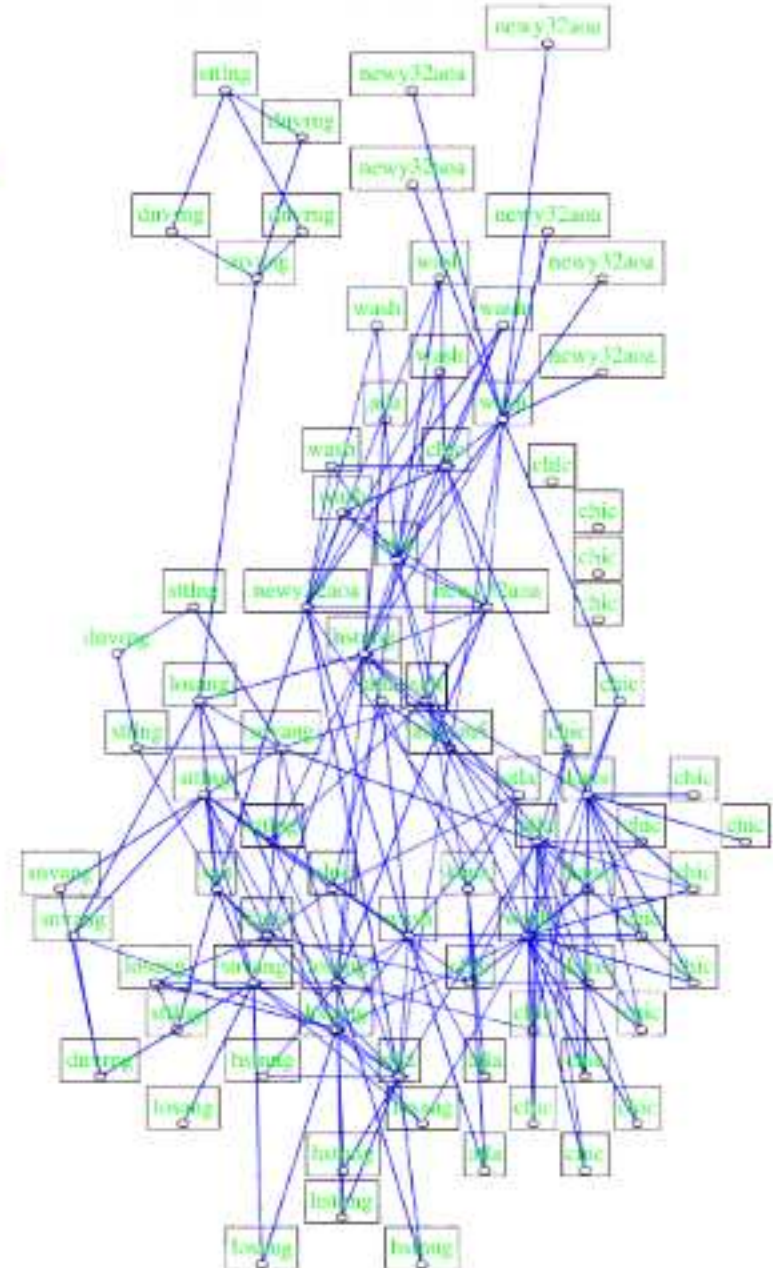
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## Actual



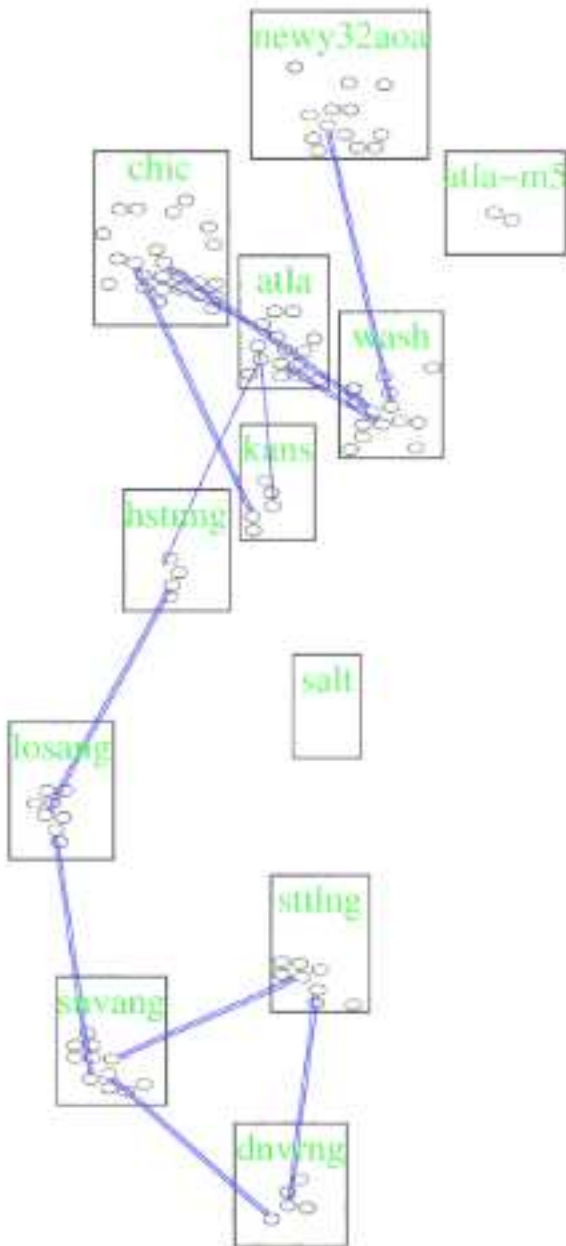
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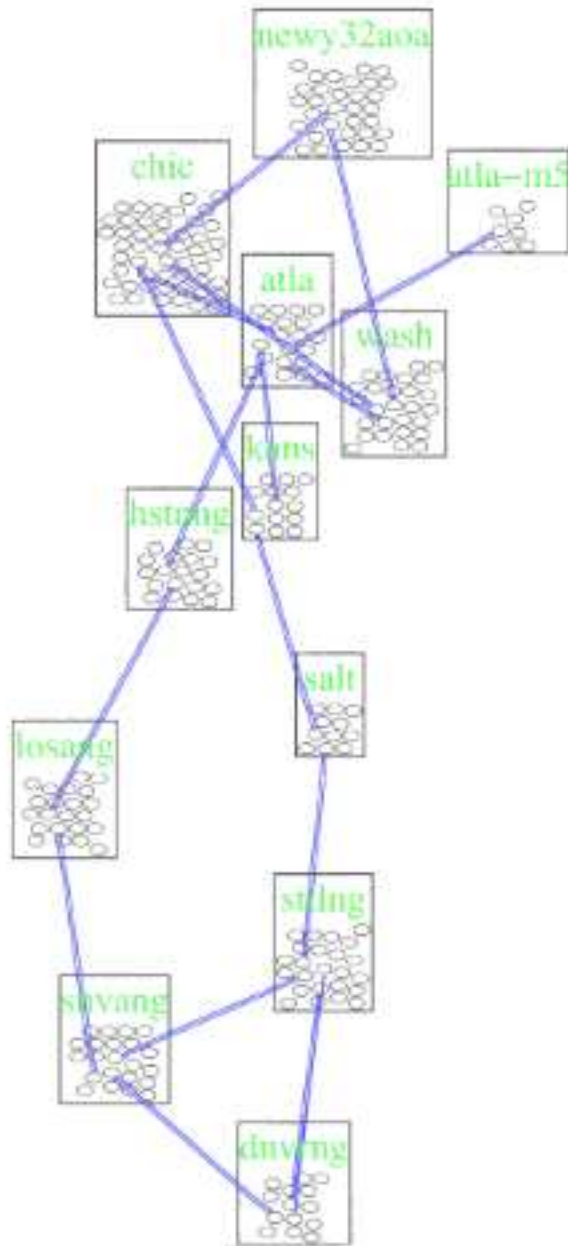


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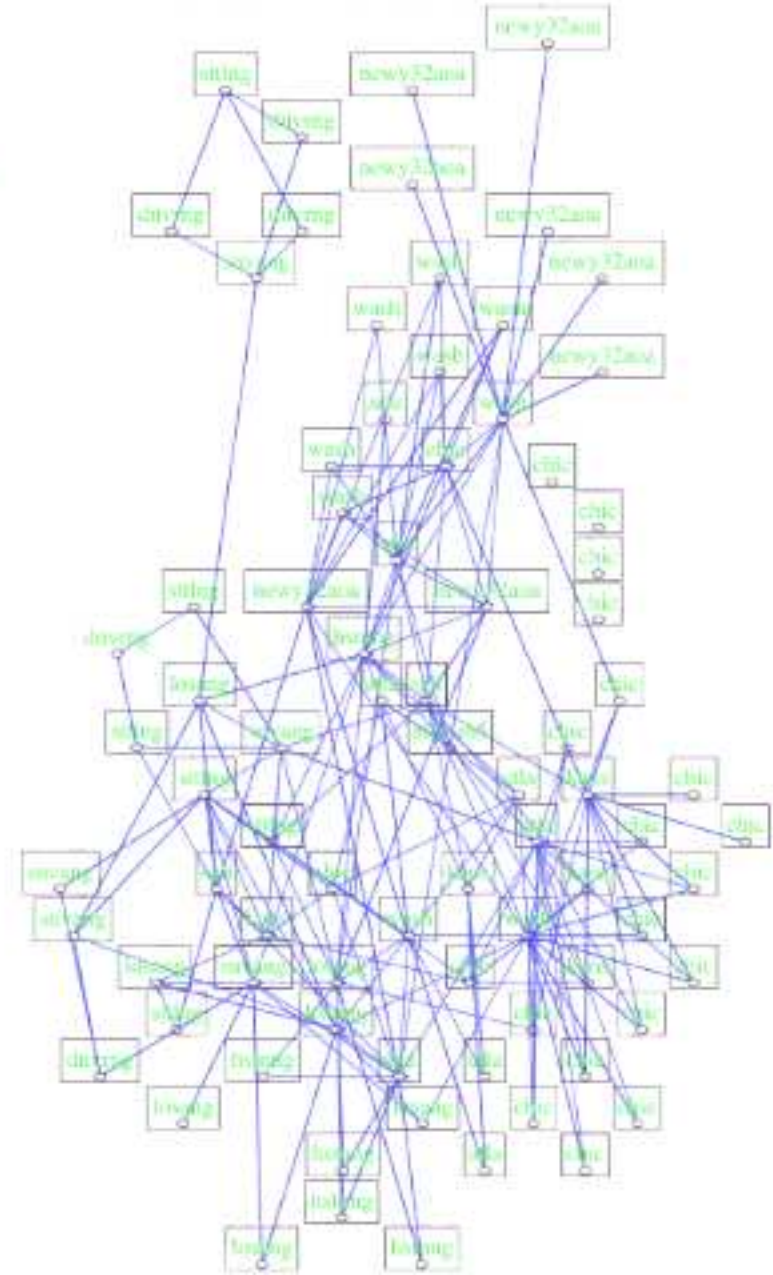
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## Actual



## Rocketfuel



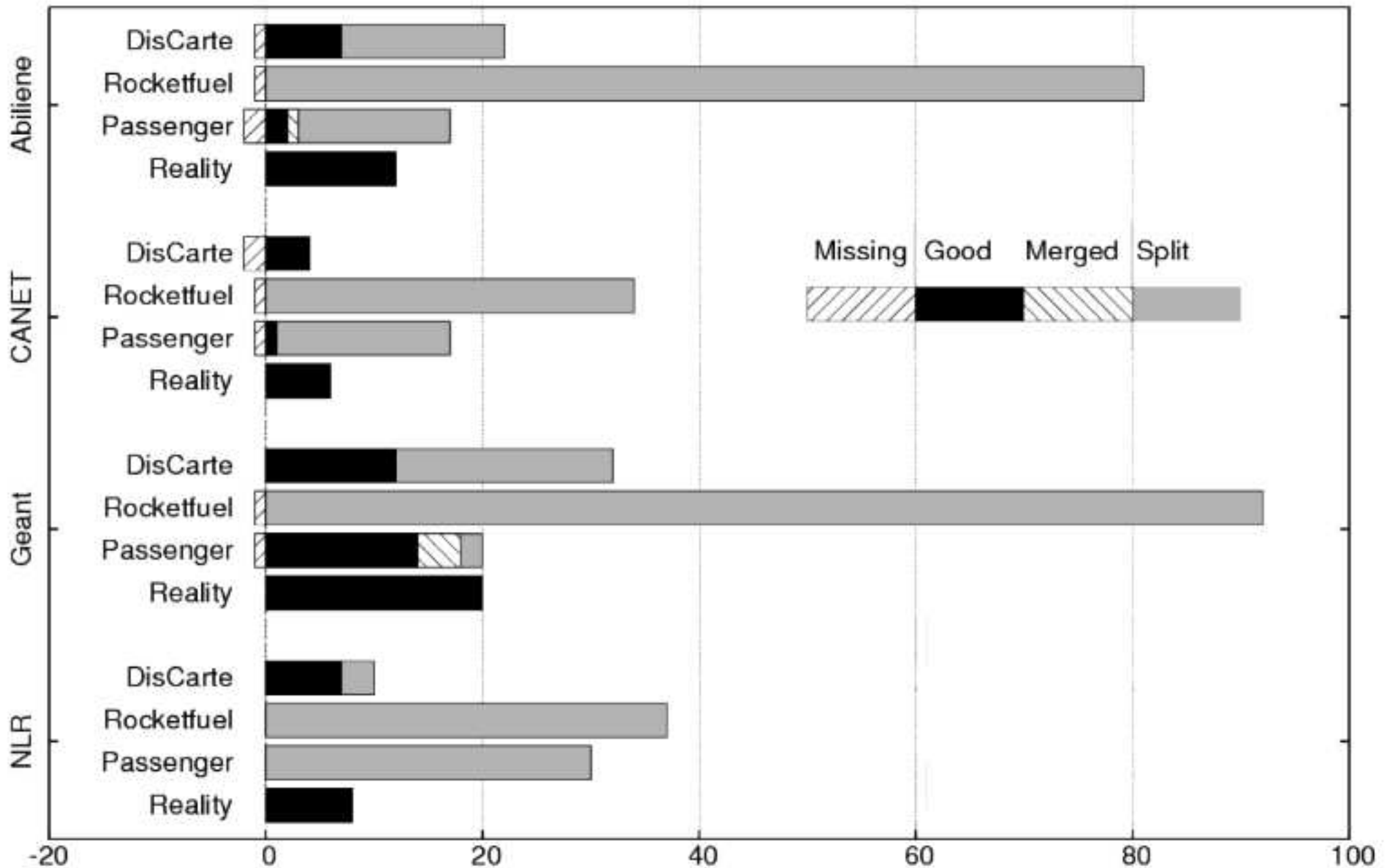
# Conclusions

- ◆ Shown how to use Record Route
- ◆ Described DisCarte
  - Cross-validates measurements to reduce error
- ◆ Introduced disjunctive logic programming (DLP)
  - Developed divide and conquer scheme to scale DLP to Internet-sized topologies
- ◆ Shown DisCarte was more accurate than previous techniques

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# Published Networks



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